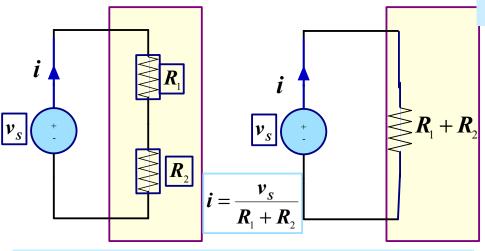






## THE CONCEPT OF EQUIVALENT CIRCUIT

THIS CONCEPT WILL OFTEN BE USED TO SIMPLFY THE ANALYSIS OF CIRCUITS. WE INTRODUCE IT HERE WITH A VERY SIMPLE VOLTAGE DIVIDER



AS FAR AS THE CURRENT IS CONCERNED BOTH CIRCUITS ARE EQUIVALENT. THE ONE ON THE RIGHT HAS ONLY ONE RESISTOR

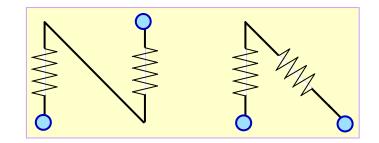
 SERIES COMBINATION OF RESISTORS

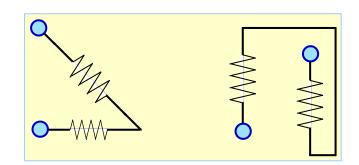
 D
 D
 D

$$-\underbrace{\mathbf{R}_{1}}_{1} \quad \underbrace{\mathbf{R}_{2}}_{2} \quad = \\ -\underbrace{\mathbf{R}_{1}}_{1} + \underbrace{\mathbf{R}_{2}}_{2}$$

THE DIFFERENCE BETWEEN ELECTRIC CONNECTION AND PHYSICAL LAYOUT

SOMETIMES, FOR PRACTICAL CONSTRUCTION REASONS, COMPONENTS THAT ARE ELECTRICALLY CONNECTED MAY BE PHYSICALLY QUITE APART

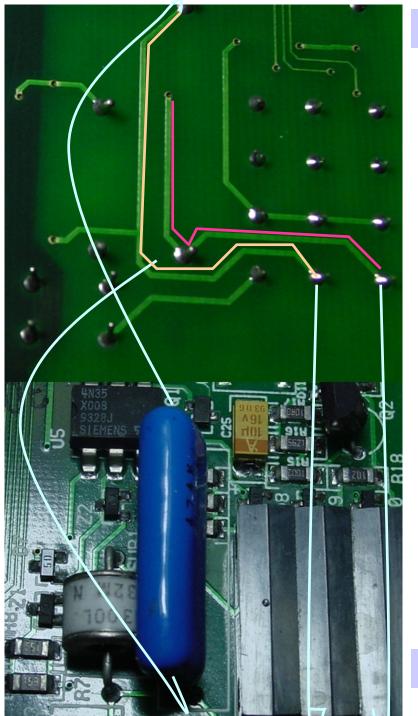




IN ALL CASES THE RESISTORS ARE CONNECTED IN SERIES







## CONNECTOR SIDE

ILLUSTRATING THE DIFFERENCE BETWEEN PHYSICAL LAYOUT AND ELECTRICAL CONNECTIONS

PHYSICAL NODE

PHYSICAL NODE

SECTION OF 14.4 KB VOICE/DATA MODEM

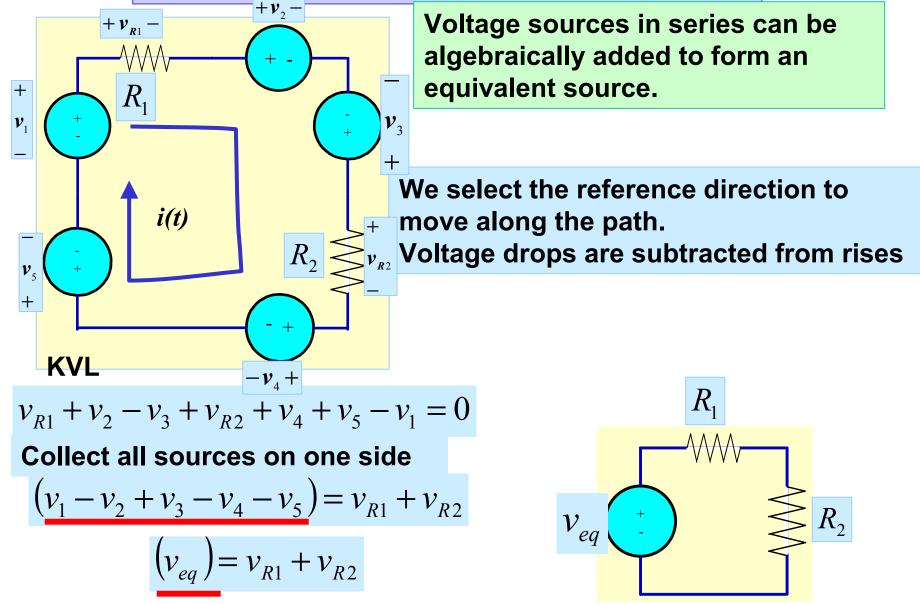
CORRESPONDING POINTS







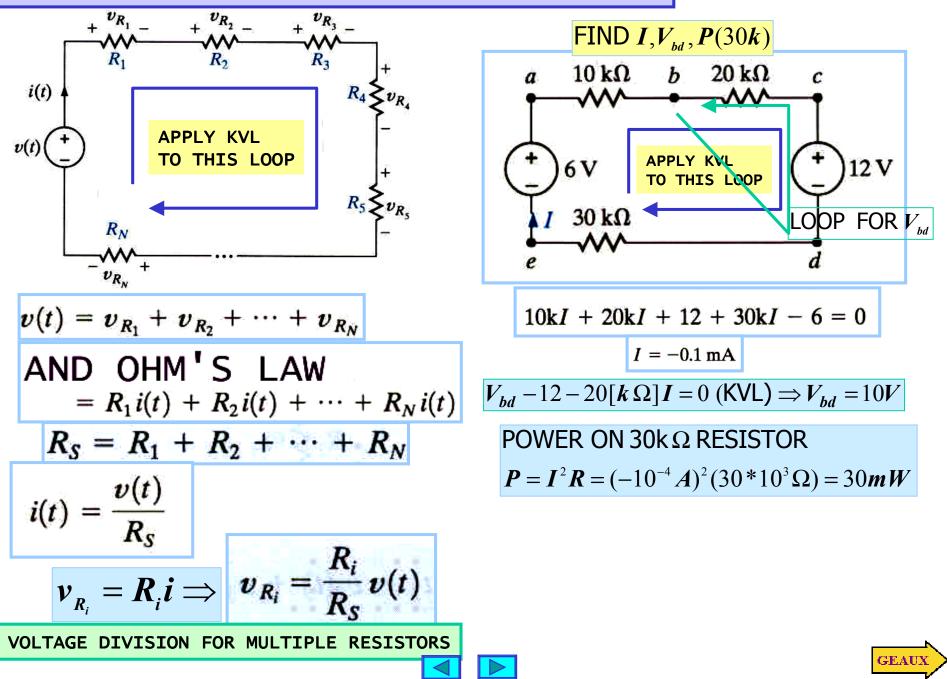
## FIRST GENERALIZATION: MULTIPLE SOURCES

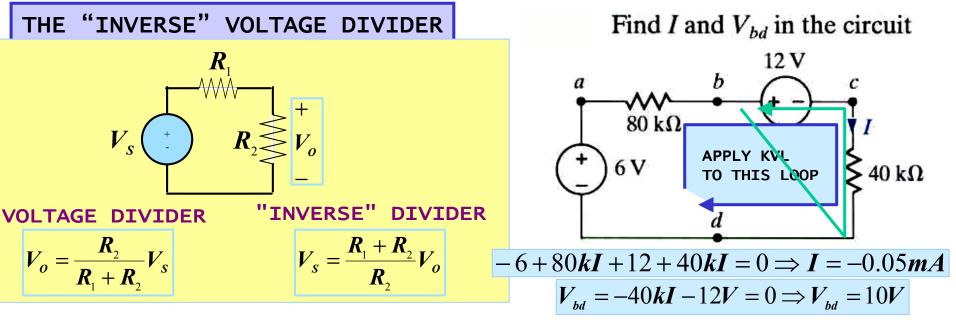


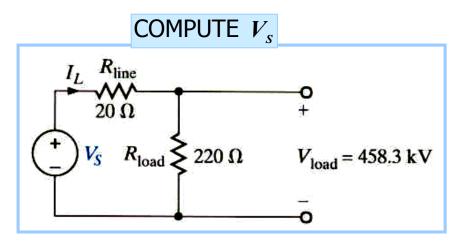




SECOND GENERALIZATION: MULTIPLE RESISTORS

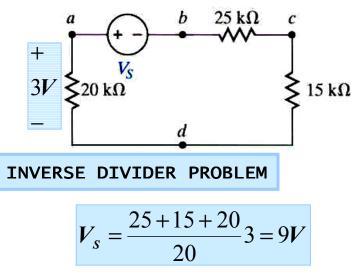






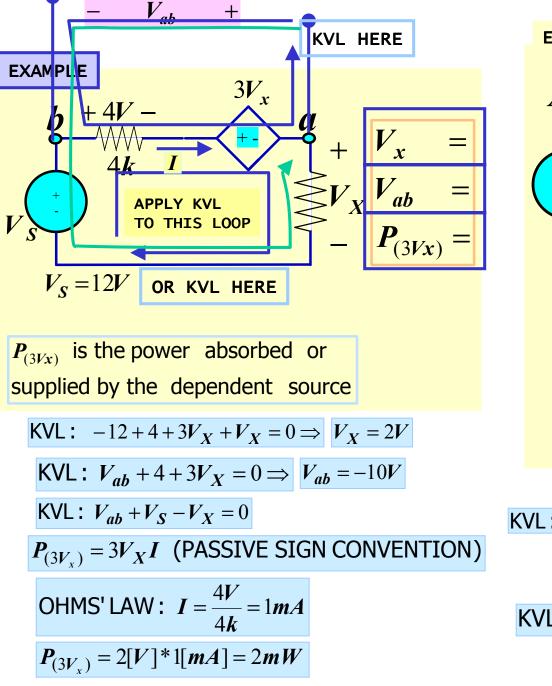
"INVERSE" DIVIDER $V_{s} = \frac{220 + 20}{220} 458.3 = 500 k\Omega$ 

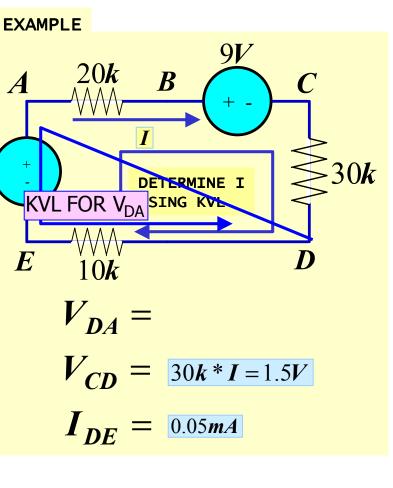
In the network in Fig. E2.11, if  $V_{ad}$  is 3 V, find  $V_S$ .











KVL: 
$$-12 + 20k * I + 9 + 30k * I + 10k * I = 0$$
  
 $I = \frac{3V}{60k\Omega} = 0.05mA$   
KVL:  $V_{DA} + 12 - 10k * I = 0$   
 $V_{DA} = -11.5V$ 

STUP