CAPACITANCE AND INDUCTANCE

Introduces two passive, energy storing devices: Capacitors and Inductors

LEARNING GOALS

CAPACITORS Store energy in their electric field (electrostatic energy) Model as circuit element

INDUCTORS Store energy in their magnetic field Model as circuit element

CAPACITOR AND INDUCTOR COMBINATIONS Series/parallel combinations of elements

RC OP-AMP CIRCUITS Integration and differentiation circuits

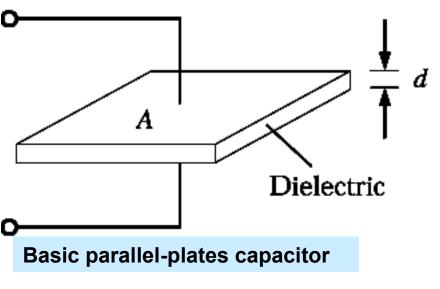


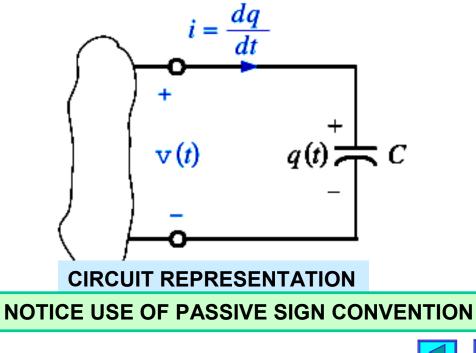


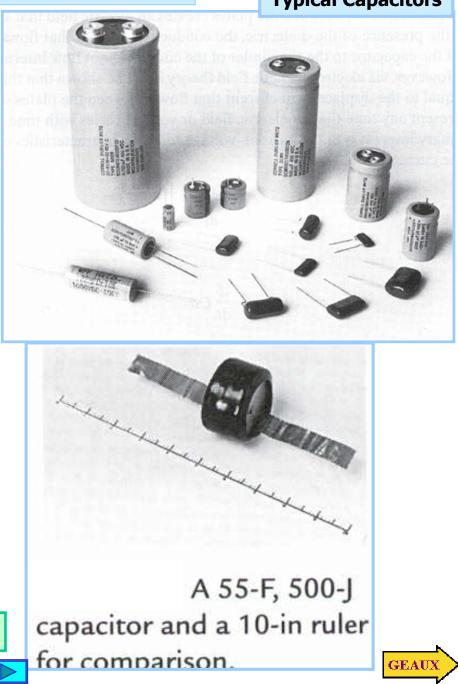
CAPACITORS

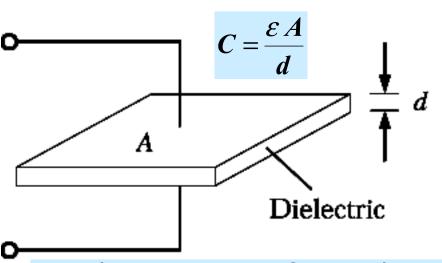
First of the energy storage devices to be discussed

Typical Capacitors









 ε Dielectric constant of material in gap

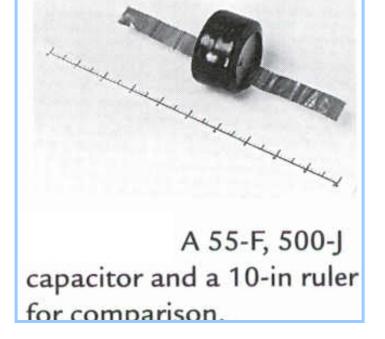


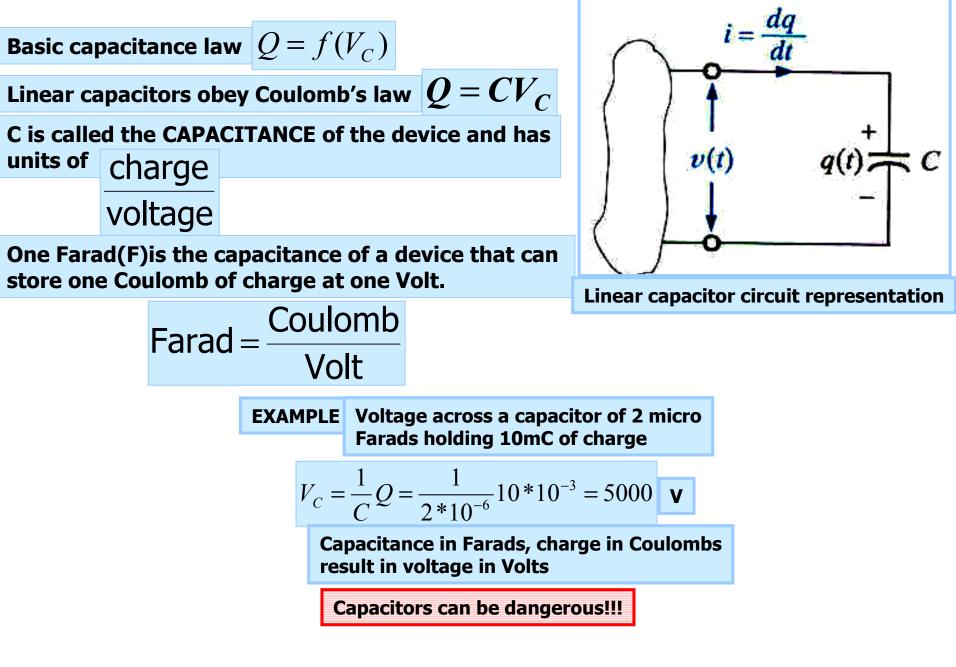
PLATE SIZE FOR EQUIVALENT AIR-GAP CAPACITOR

$$55F = \frac{8.85 \times 10^{-12} A}{1.016 \times 10^{-4}} \Longrightarrow A = 6.3141 \times 10^8 m^2$$

Normal values of capacitance are small. Microfarads is common. For integrated circuits nano or pico farads are not unusual





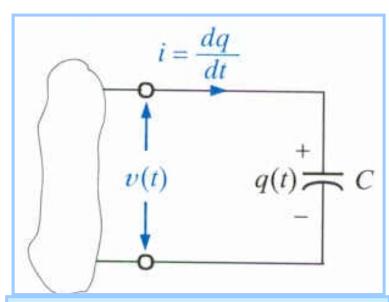






Capacitors only store and release ELECTROSTATIC energy. They do not "create"

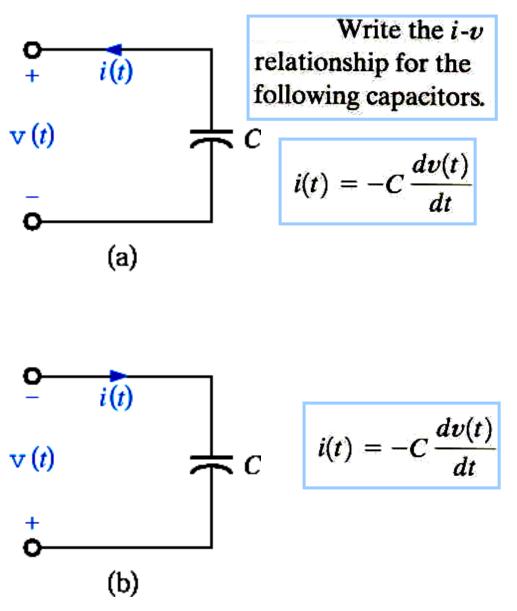
The capacitor is a passive element and follows the passive sign convention



Linear capacitor circuit representation

$$i(t) = C \frac{dv}{dt}(t)$$

LEARNING BY DOING







 $Q_C = CV_C$ Capacitance Law

If the voltage varies the charge varies and there is a displacement current

One can also express the voltage across in terms of the current

$$V_C(t) = \frac{1}{C}Q = \frac{1}{C}\int_{-\infty}^t i_C(x)dx$$

Integral form of Capacitance law

The mathematical implication of the integral form is ...

$$V_C(t-) = V_C(t+); \forall t$$

Voltage across a capacitor MUST be continuous ... Or one can express the current through in terms of the voltage across

$$i_C = \frac{dQ}{dt} = C \frac{dV_C}{dt}$$

Differential form of Capacitance law

Implications of differential form??

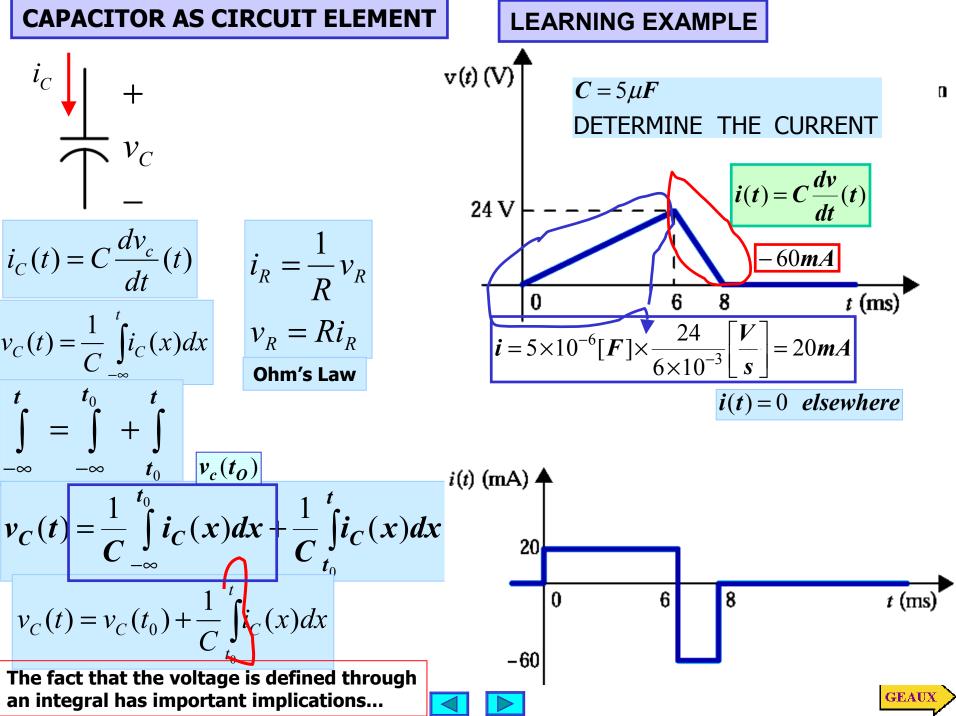
 $V_C = Const \Rightarrow i_C = 0$

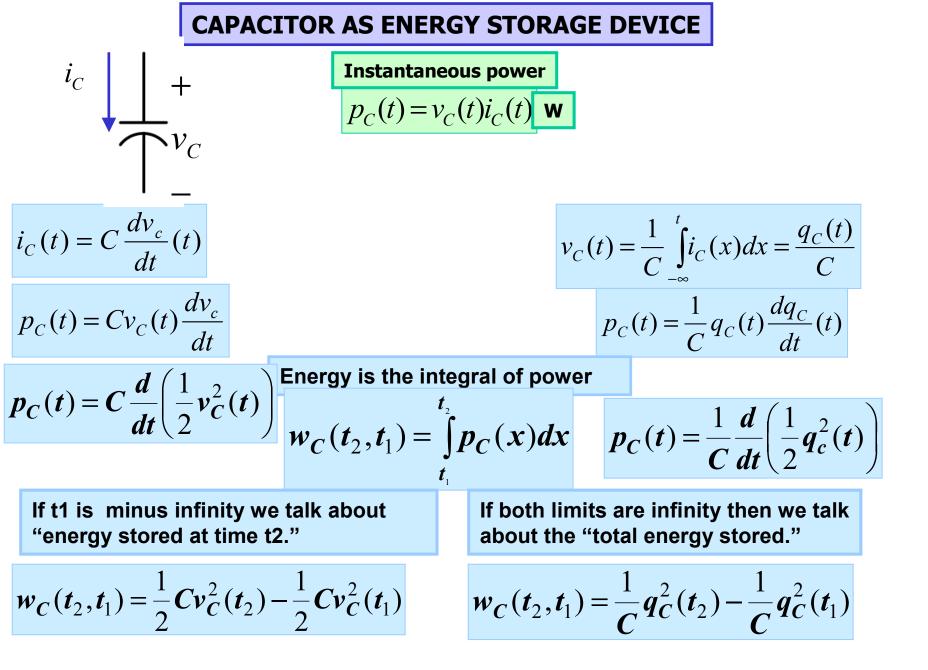
DC or steady state behavior

A capacitor in steady state acts as an OPEN CIRCUIT



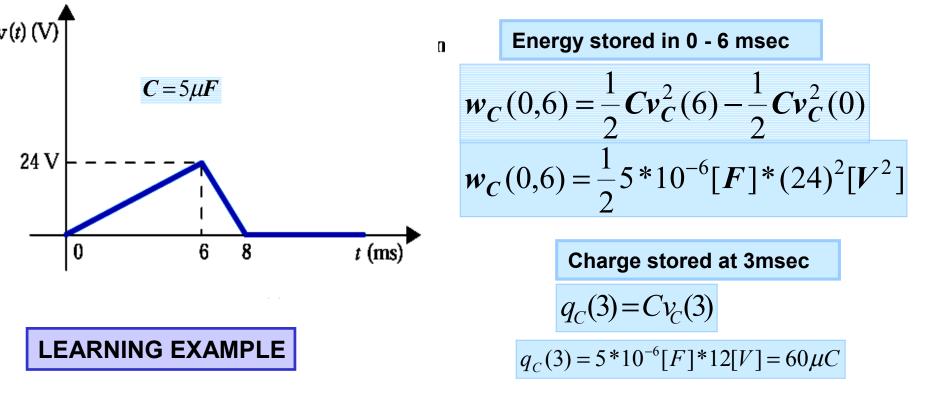












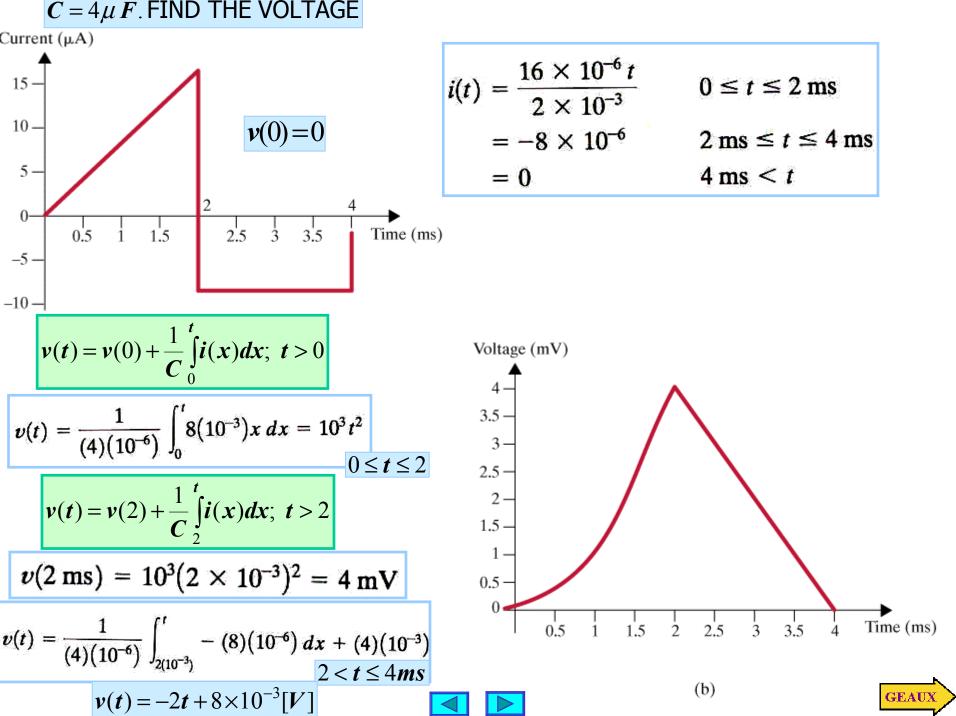
"total energy stored?"

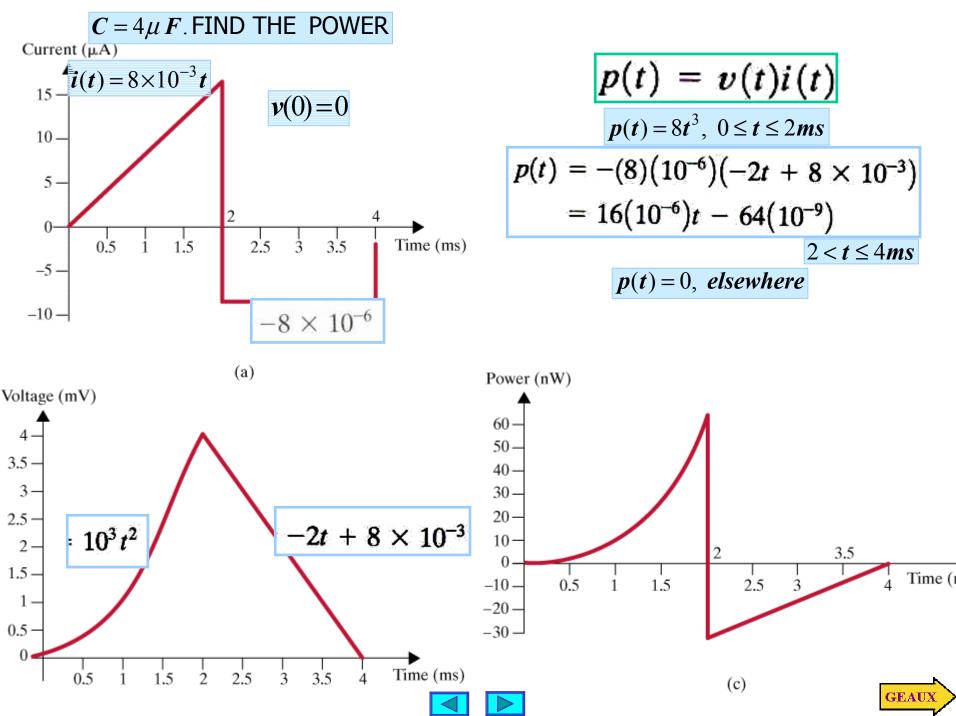
"total charge stored?" ...

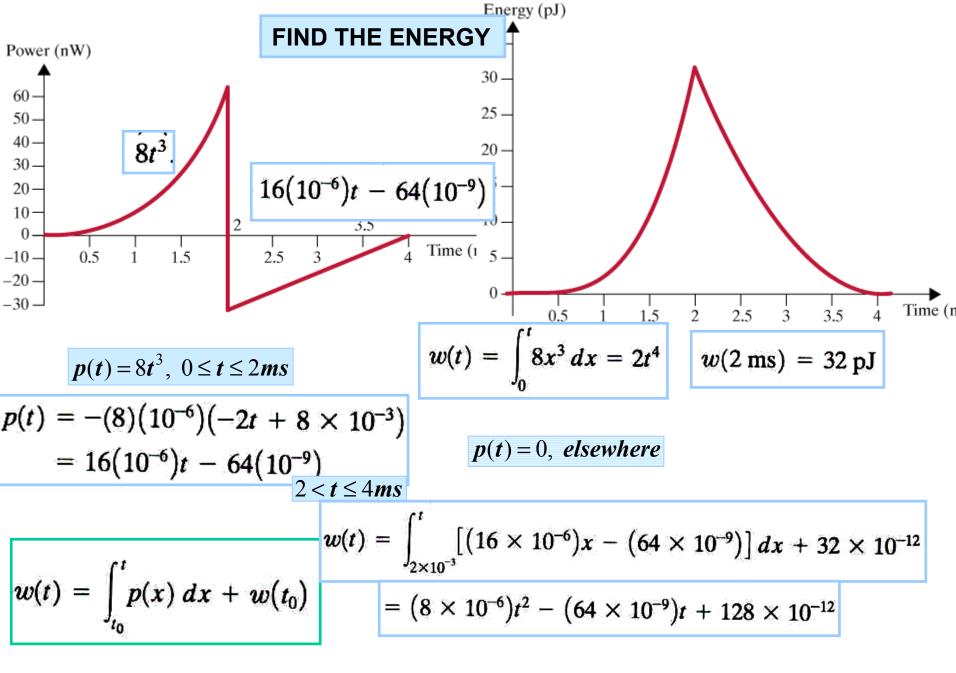
If charge is in Coulombs and capacitance in Farads then the energy is in





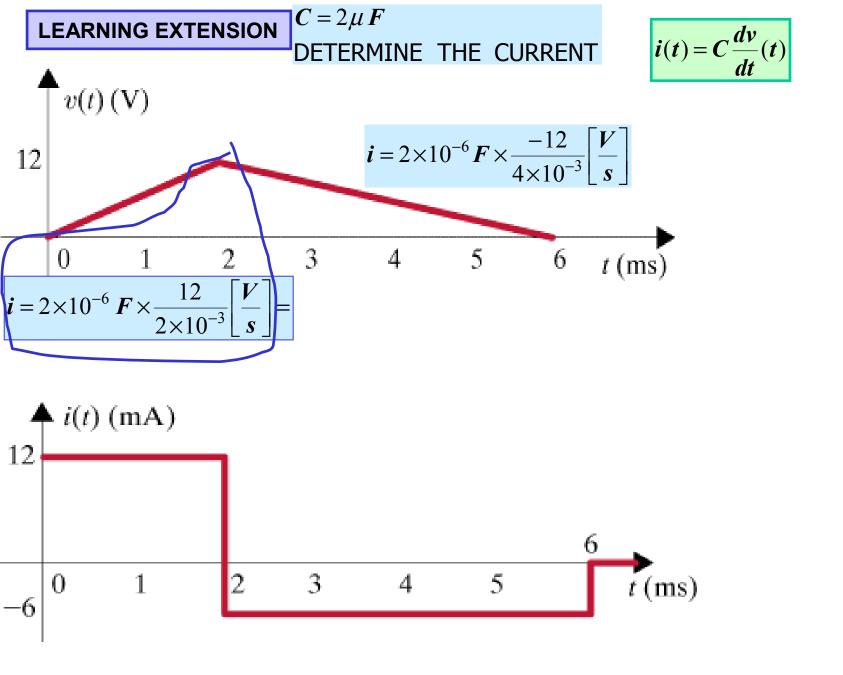






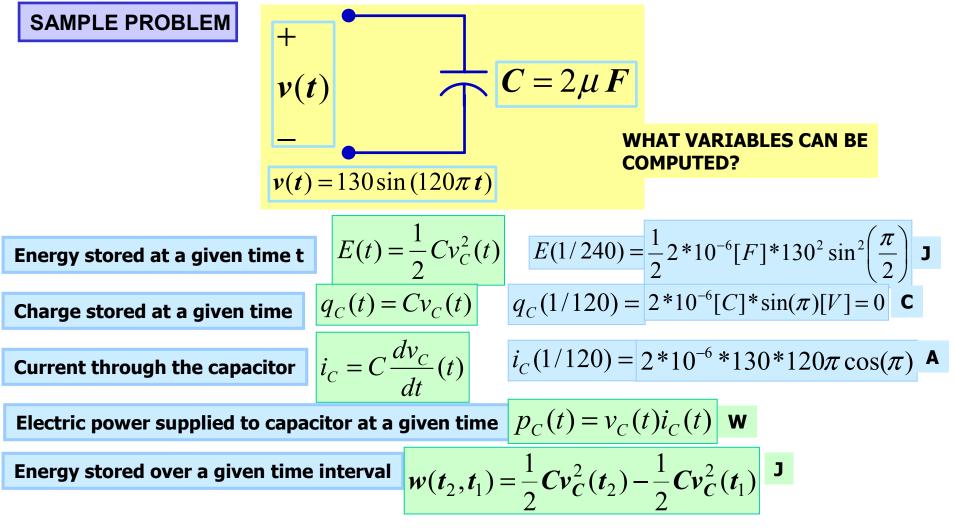






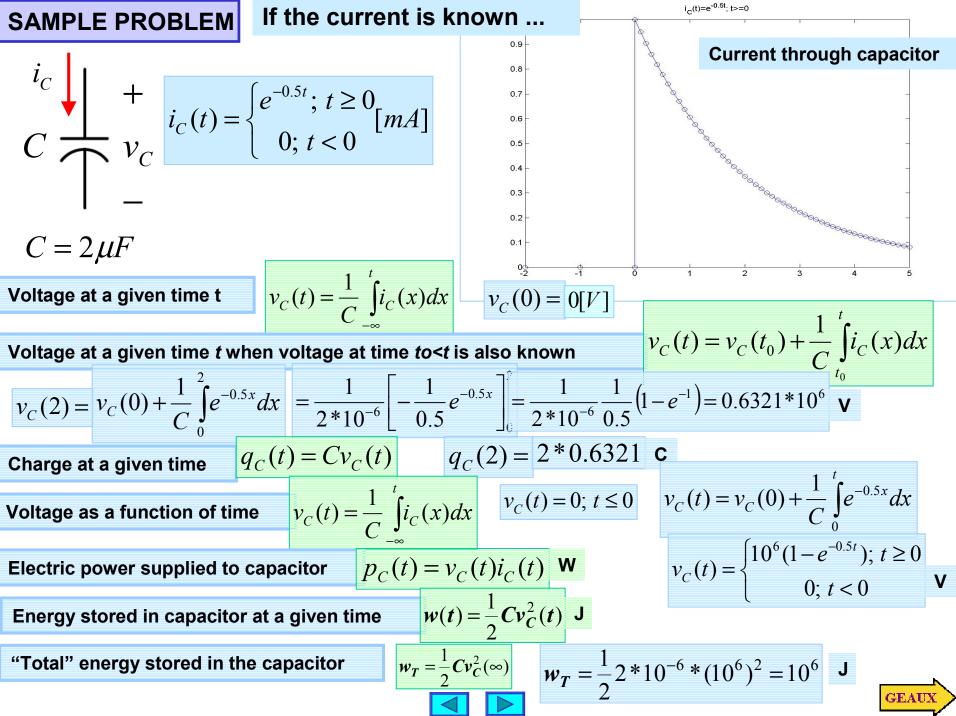


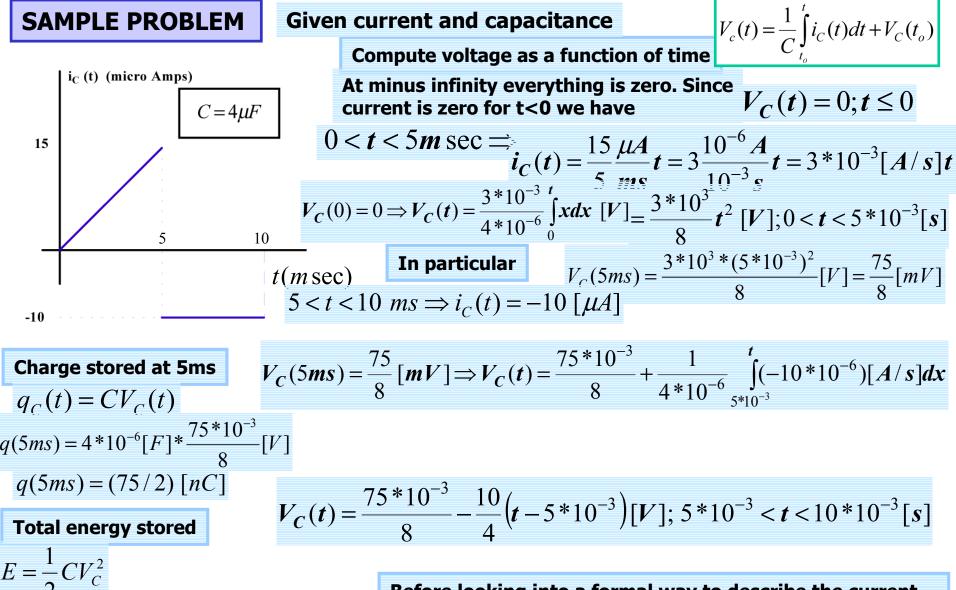












Total means at infinity. Hence

 $E_T = 0.5 * 4 * 10^{-6} \left(\frac{25 * 10^{-3}}{8}\right)^2 [J]$

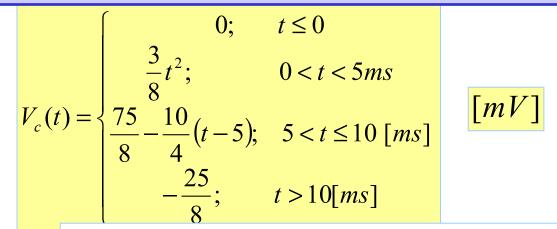
Before looking into a formal way to describe the current we will look at additional questions that can be answered.

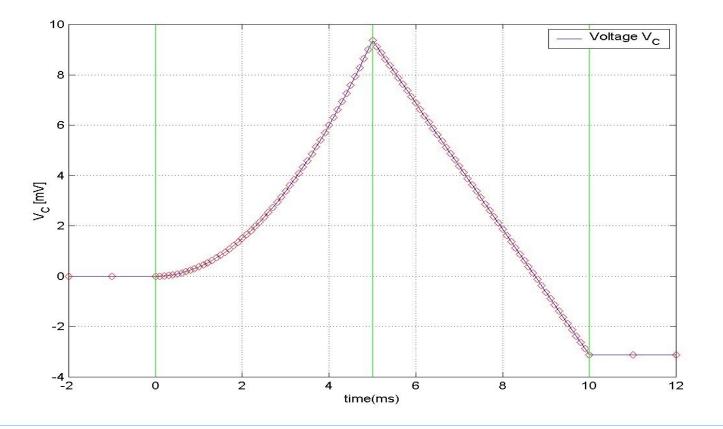
GEAUX

Now, for a formal way to represent piecewise functions....



Formal description of a piecewise analytical signal



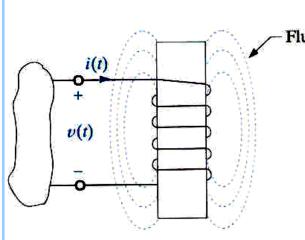




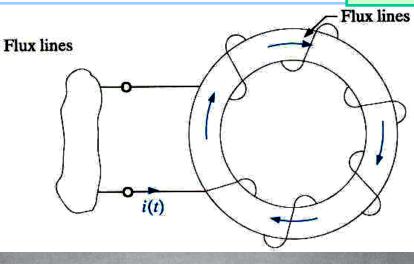




NOTICE USE OF PASSIVE SIGN CONVENTION

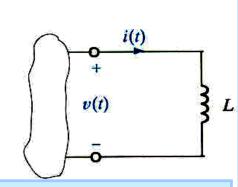


Flux lines may extend beyond inductor creating stray inductance effects



Coilcraft

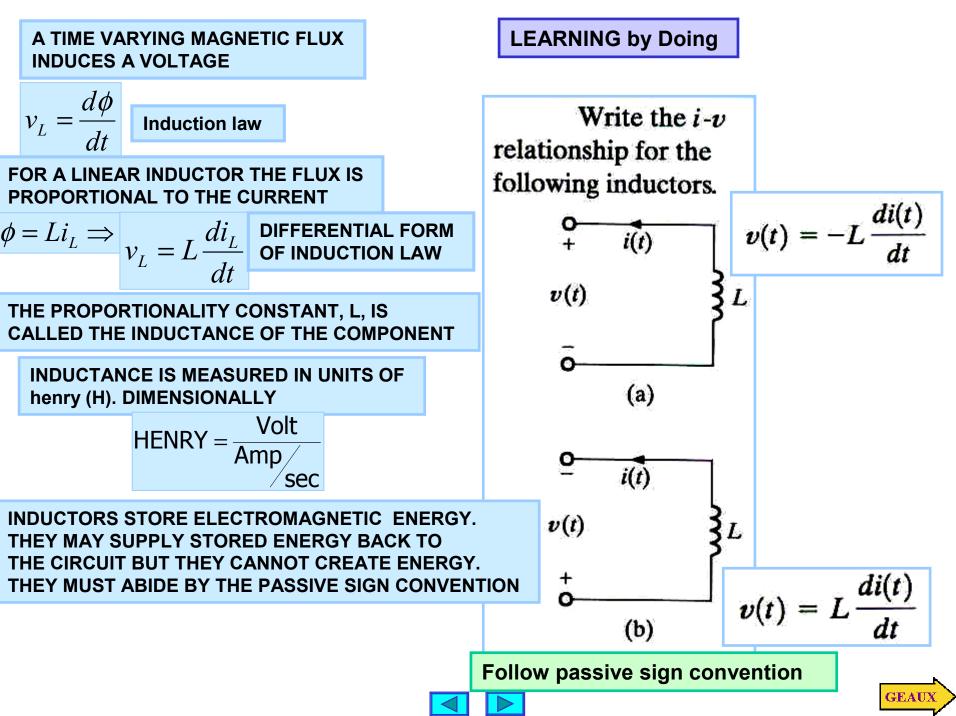
Coilcraft



Circuit representation for an inductor

A TIME VARYING FLUX CREATES A COUNTER EMF AND CAUSES A VOLTAGE TO APPEAR AT THE TERMINALS OF THE DEVICE





$$i(t)$$

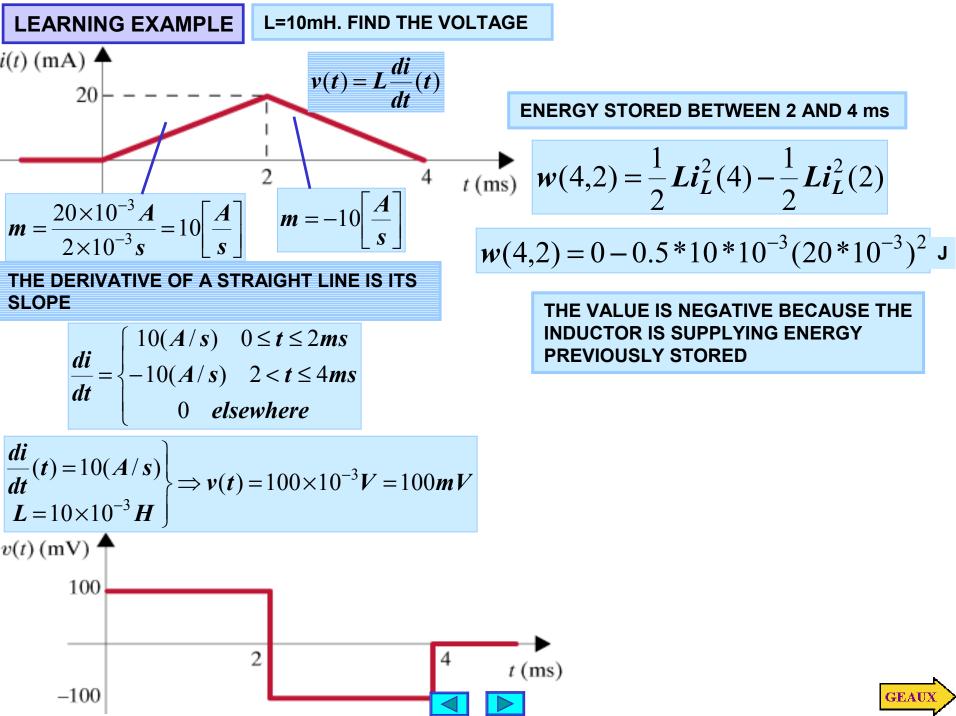
$$v_{L} = L \frac{di_{L}}{dt}$$
Differential form of induction law
$$v_{L} = L \frac{di_{L}}{dt}$$

$$v_{L} = L \frac{di_{L}}{dt}$$
Integral form of induction law
$$i_{L}(t) = \frac{1}{L} \int_{-\infty}^{t} v_{L}(x) dx$$
Integral form of induction law
$$i_{L}(t) = \frac{1}{L} \int_{-\infty}^{t} v_{L}(x) dx$$
Integral form of induction law
$$i_{L}(t) = \frac{1}{L} \int_{t_{0}}^{t} v_{L}(x) dx; t \ge t_{0}$$
A direct consequence of integral form
$$i_{L}(t) = i_{L}(t_{0}) + \frac{1}{L} \int_{t_{0}}^{t} v_{L}(x) dx; t \ge t_{0}$$
A direct consequence of differential form
$$i_{L}(t) = i_{L}(t_{1}); \forall t \text{ Current MUST be continuous}$$
A direct consequence of differential form
$$i_{L} = Const. \Rightarrow v_{L} = 0 \text{ DC (steady state) behavior}$$
Power and Energy stored
$$p_{L}(t) = v_{L}(t)i_{L}(t) \text{ W} \quad p_{L}(t) = L \frac{di_{L}}{dt}(t)i_{L}(t) = \frac{d}{dt}(\frac{1}{2}Li_{L}^{2}(t))$$

$$w_{L}(t_{2}, t_{1}) = \int_{t_{L}}^{t} \frac{d}{dt}(\frac{1}{2}Li_{L}^{2}(x)) dx \text{ J Current in Amps, Inductance in Henrys yield energy in Joules}$$

$$w(t_{2}, t_{1}) = \frac{1}{2}Li_{L}^{2}(t_{2}) - \frac{1}{2}Li_{L}^{2}(t_{1}) \text{ Energy stored on the interval Can be positive or negative}$$

$$w_{L}(t) = \frac{1}{2}Li_{L}^{2}(t) \text{ Was be non-negative. Passive element!!! }$$



SAMPLE PROBLEM L=0.1H, i(0)=2A. Find i(t), t>0

ENERGY COMPUTATIONS

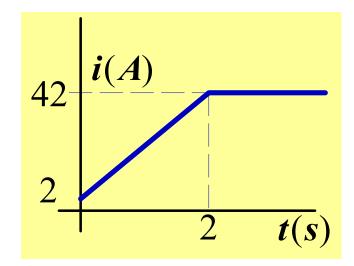
$$w(t_2, t_1) = \frac{1}{2} L i_L^2(t_2) - \frac{1}{2} L i_L^2(t_1)$$

Energy stored on the interval Can be positive or negative

$$\frac{2}{i(t) = i(0) + \frac{1}{L} \int_{0}^{t} v(x) dx}$$

$$\frac{2}{L} \frac{1}{t(s)}$$

$$v(x) = 2 \Rightarrow \int_{0}^{t} v(x) dx = 2t; \quad 0 < t \le 2$$
$$L = 0.1H \Rightarrow i(t) = 2 + 20t; \quad 0 \le t \le 2s$$
$$v(x) = 0; \quad t > 2 \Rightarrow i(t) = i(2); \quad t > 2s$$



Initial energy stored in inductor $w(0) = 0.5 * 0.1[H](2A)^2 = 0.2[J]$

"Total energy stored in the inductor" $w(\infty) = 0.5 * 0.1 [H] * (42A)^2 = 88.2J$

Energy stored between 0 and 2 sec $w(2,0) = \frac{1}{2}Li_{L}^{2}(2) - \frac{1}{2}Li_{L}^{2}(0)$ $w(2,0) = 0.5*0.1*(42)^{2} - 0.5*0.1*(2)^{2}$ w(2,0) = 88[J]



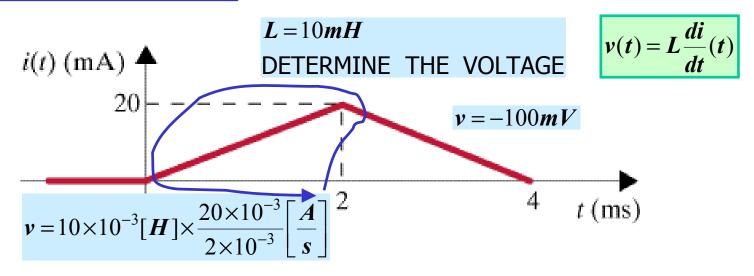


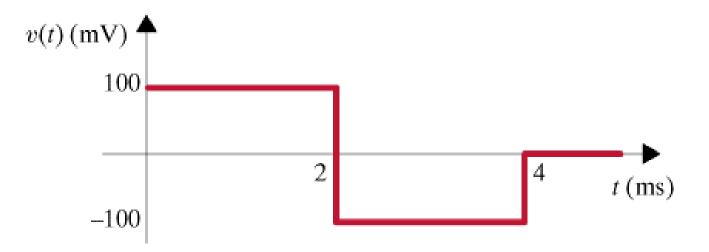
LEARNING EXAMPLEThe current in a 2-mH inductor is
$$i(t) = 2 \sin 377t \, \mathrm{A}$$
FIND THE VOLTAGE ACROSS AND THE ENERGY
STORED (AS FUNCTION OF TIME) $v(t) = L \frac{di(t)}{dt} = (2 \times 10^{-3}) \frac{d}{dt} (2 \sin 377t)$ $v(t) = L \frac{di(t)}{dt} = (2 \times 10^{-3}) \frac{d}{dt} (2 \sin 377t)$ FOR ENERGY STORED IN THE INDUCTOR $w_L(t) = \frac{1}{2} Li^2(t)$ $w_L(t) = \frac{1}{2} (2 \times 10^{-3}) (2 \sin 377t)^2$ $= 0.004 \sin^2 377t$ NOTICE THAT ENERGY STORED AT
ANY GIVEN TIME IS NON NEGATIVE
-THIS IS A PASSIVE ELEMENT-





LEARNING EXAMPLE

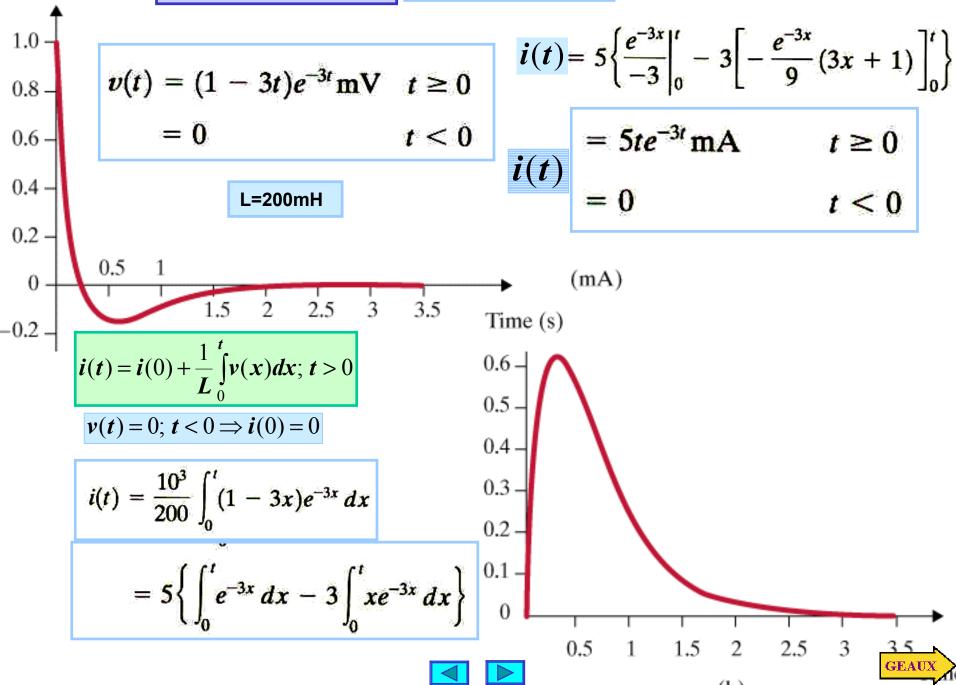


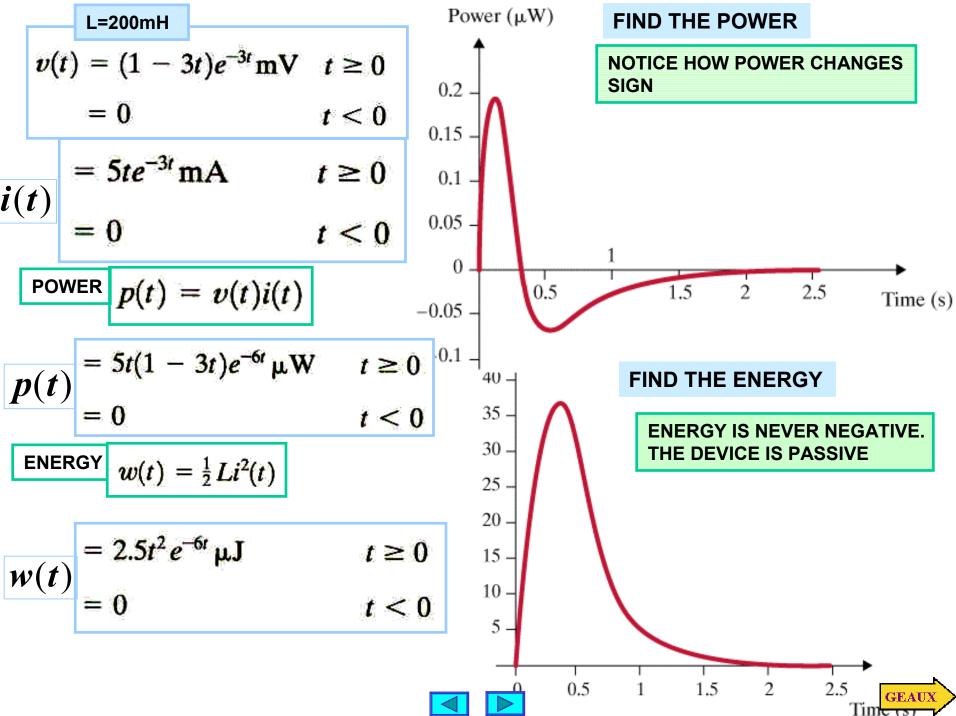




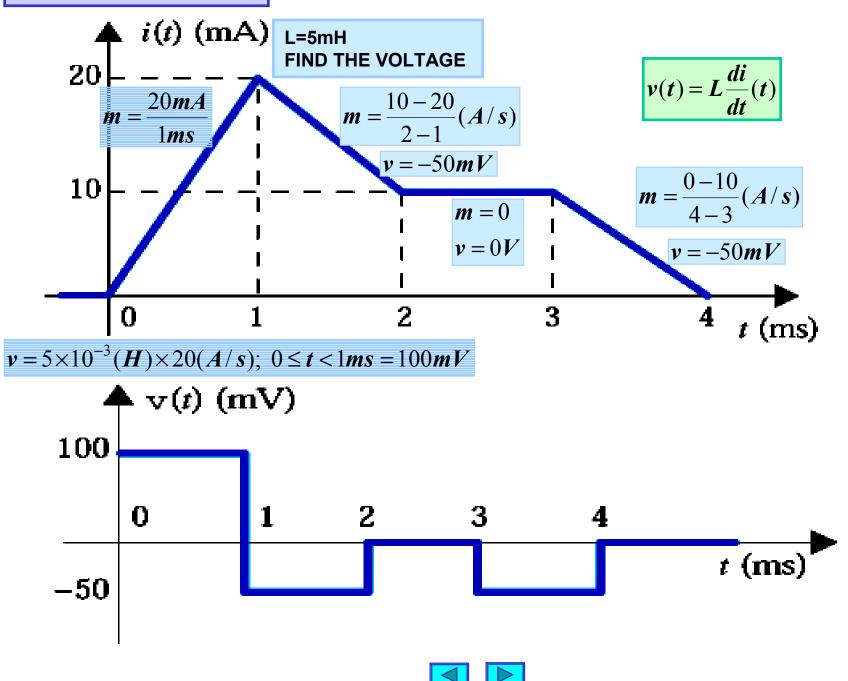




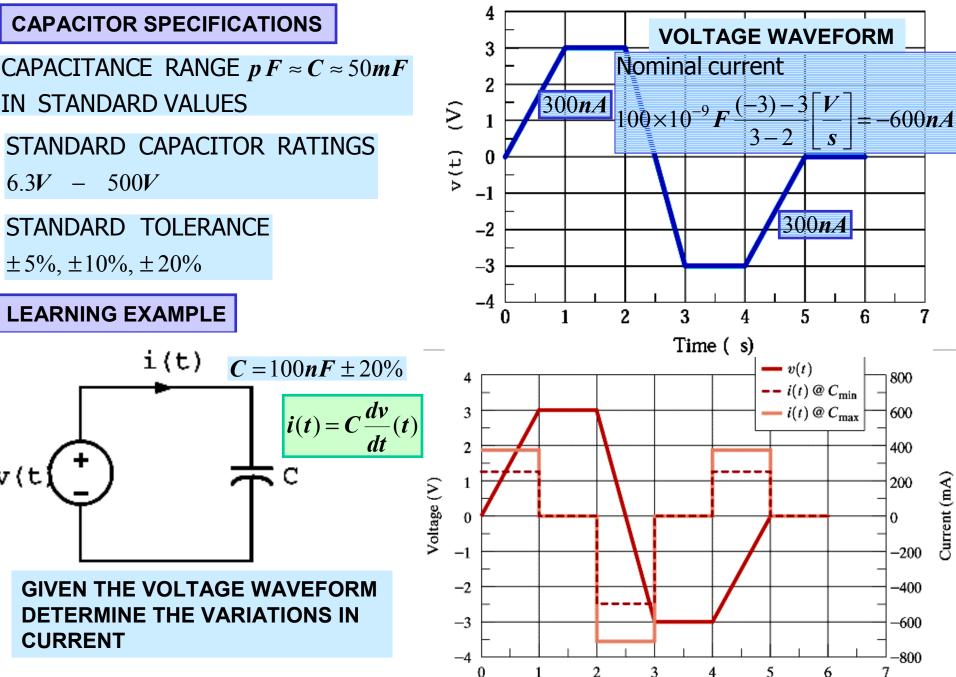




LEARNING EXTENSION



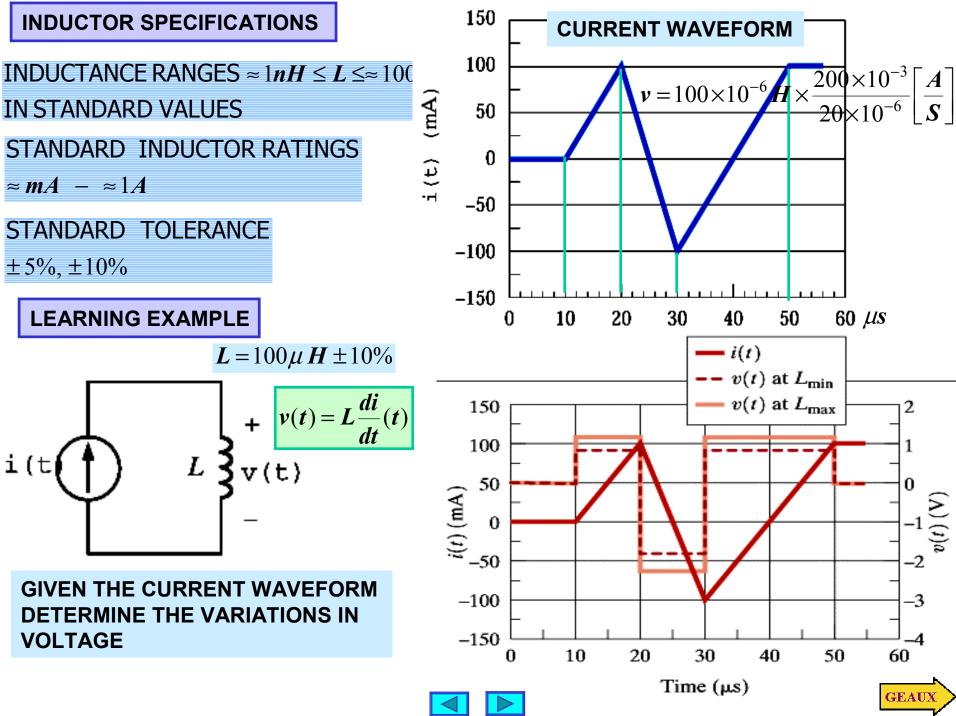






Time (µs)

GEAUX

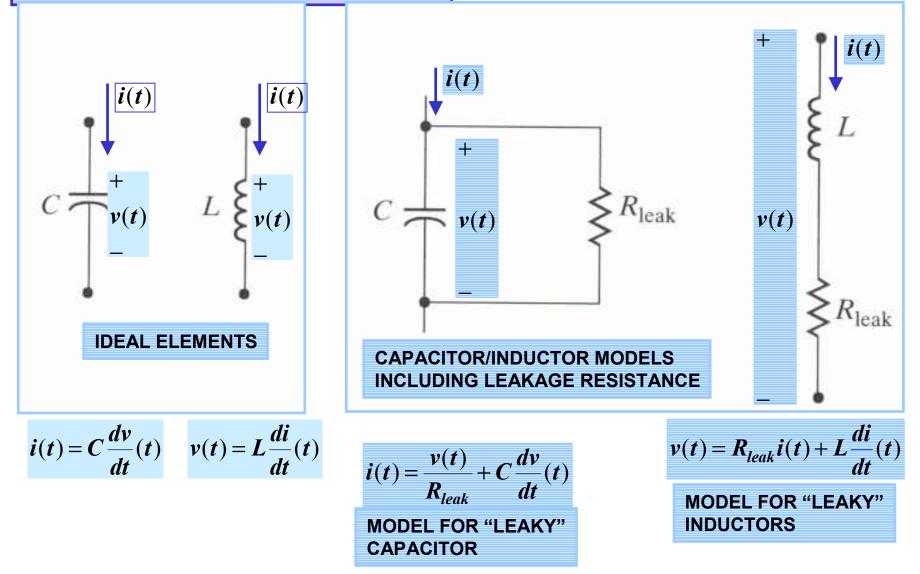


The Dual Relationship for Capacitors and Inductors		
Capacitor		Inductor
$i(t) = C \frac{dv(t)}{dt}$		$v(t) = L \frac{di(t)}{dt}$
$v(t) = \frac{1}{C} \int_{t_0}^t i(x) dx + v(t_0)$		$i(t) = \frac{1}{L} \int_{t_0}^t v(x) dx + i(t_0)$
$p(t) = Cv(t) \frac{dv(t)}{dt}$	$egin{array}{c} C ightarrow L \ v ightarrow i \end{array}$	$p(t) = Li(t)\frac{di(t)}{dt}$
$w(t) = \frac{1}{2}Cv(t)^2$	$i \rightarrow v$	$w(t)=\tfrac{1}{2}Li^2(t)$





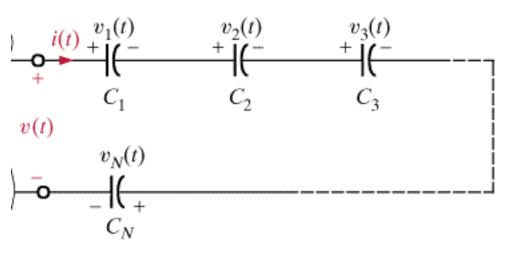
IDEAL AND PRACTICAL ELEMENTS

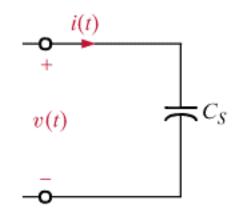






SERIES CAPACITORS





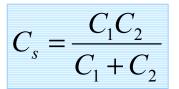
$$v(t) = v_1(t) + v_2(t) + v_3(t) + \cdots + v_N(t)$$

$$v_{i}(t) = \frac{1}{C_{i}} \int_{t_{0}}^{t} i(t) dt + v_{i}(t_{0})$$

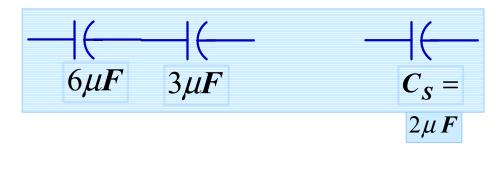
$$v(t) = \left(\sum_{i=1}^{N} \frac{1}{C_{i}}\right) \int_{t_{0}}^{t} i(t) dt + \sum_{i=1}^{N} v_{i}(t_{0})$$

$$\frac{1}{C_{s}} = \sum_{i=1}^{N} \frac{1}{C_{i}} = \frac{1}{C_{1}} + \frac{1}{C_{2}} + \dots + \frac{1}{C_{N}}$$

$$v(t_{0}) = \sum_{i=1}^{N} v_{i}(t_{0})$$
NOTICE SIMILARITY
WITH RESITORS IN
PARALLEL

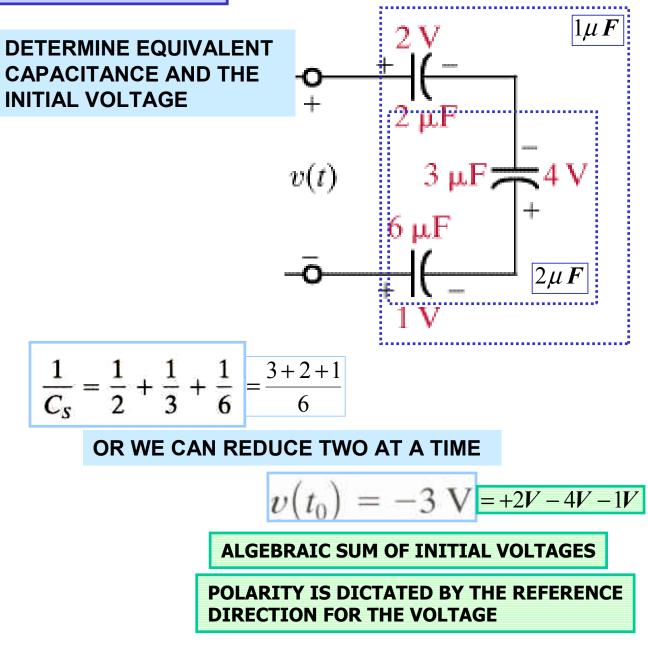


Series Combination of two capacitors







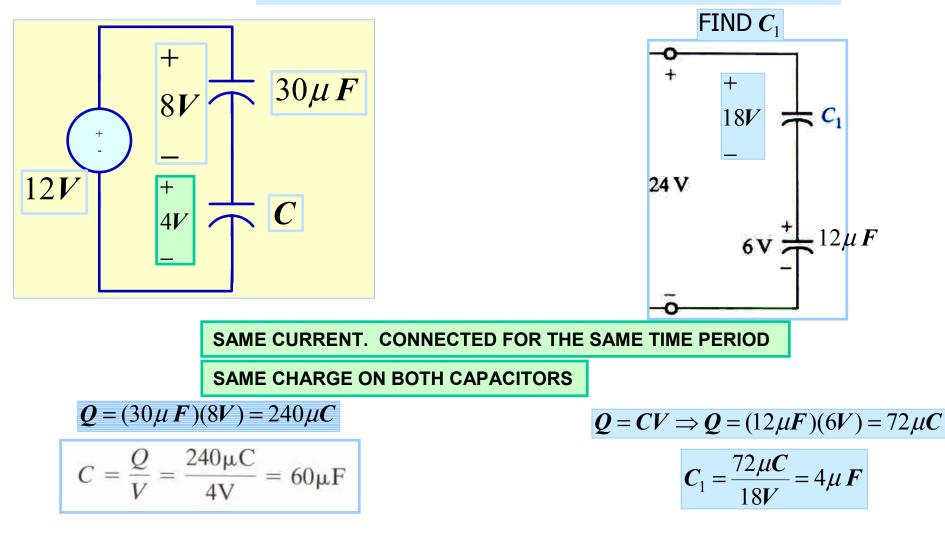






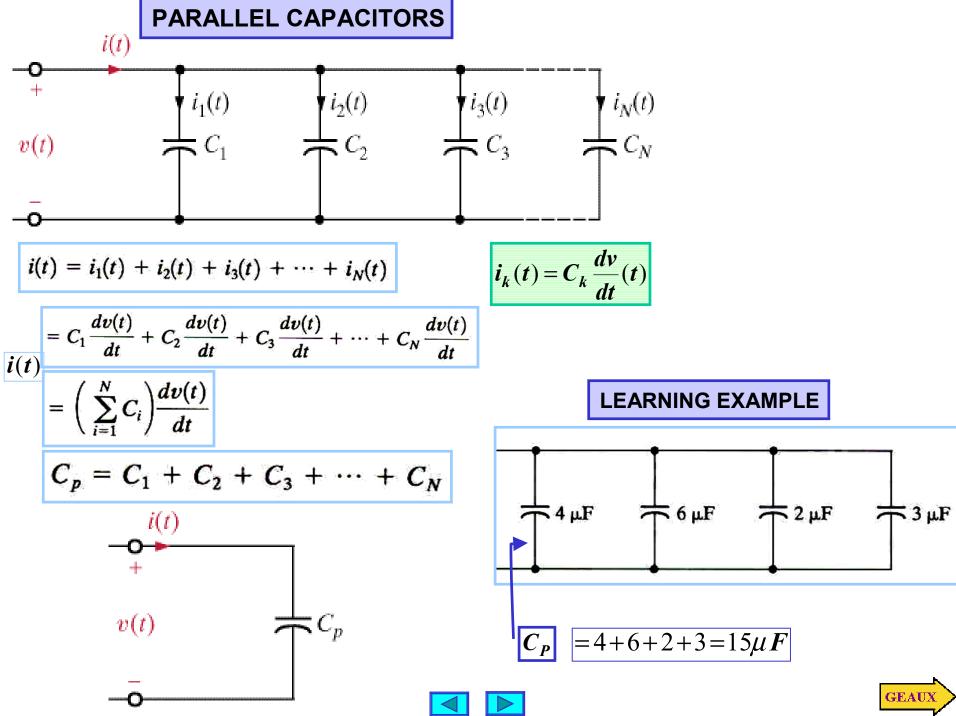
LEARNING EXAMPLE

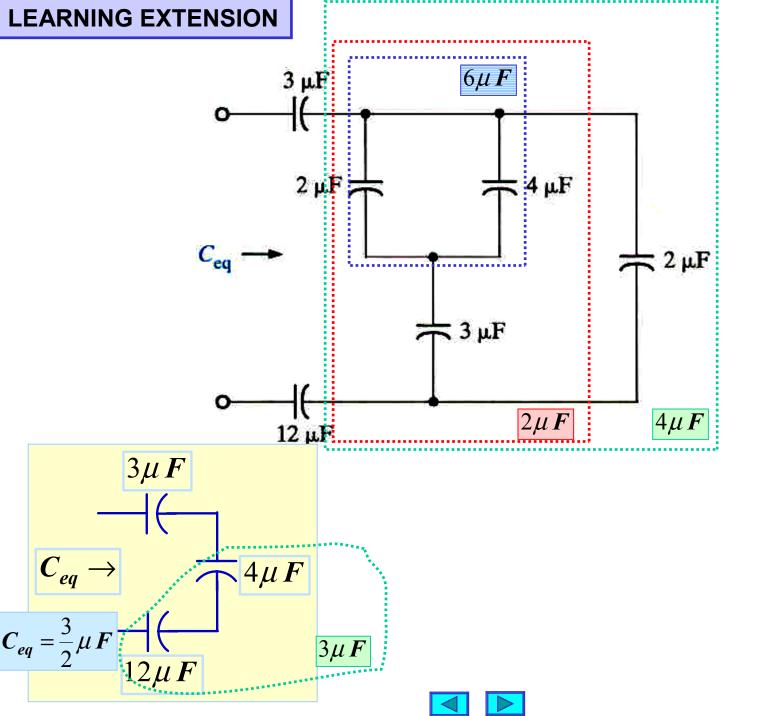
Two uncharged capacitors are connected as shown. Find the unknown capacitance



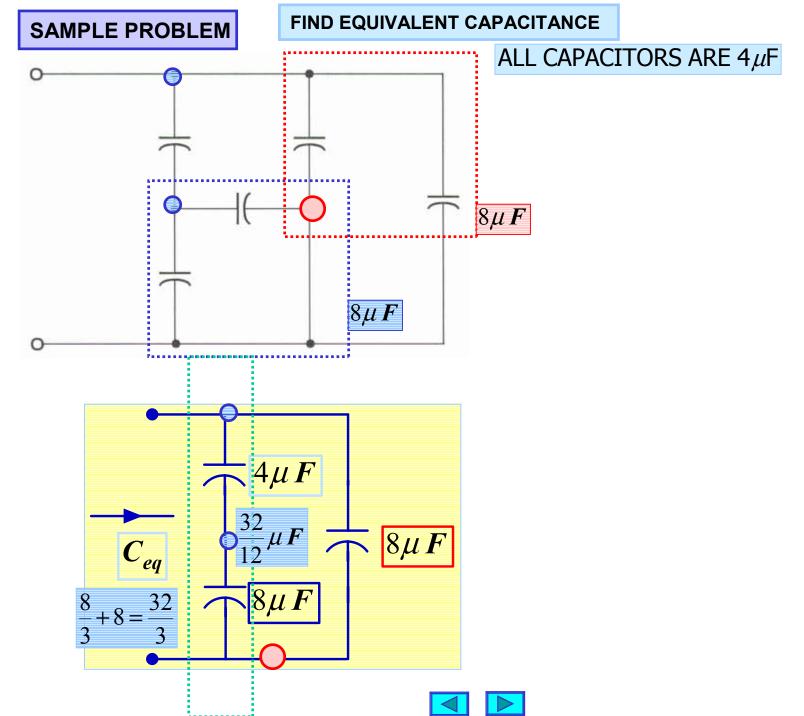






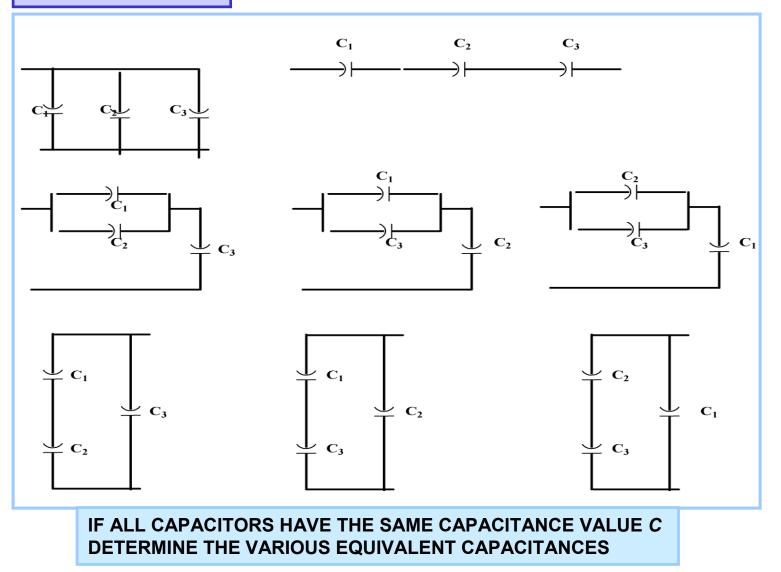






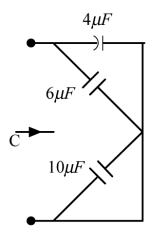


SAMPLE PROBLEM



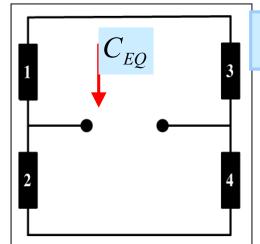




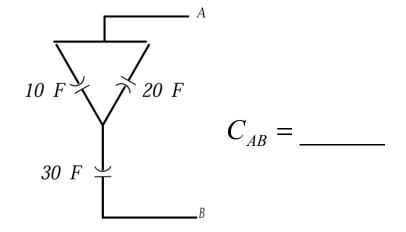


C =

Examples of interconnections



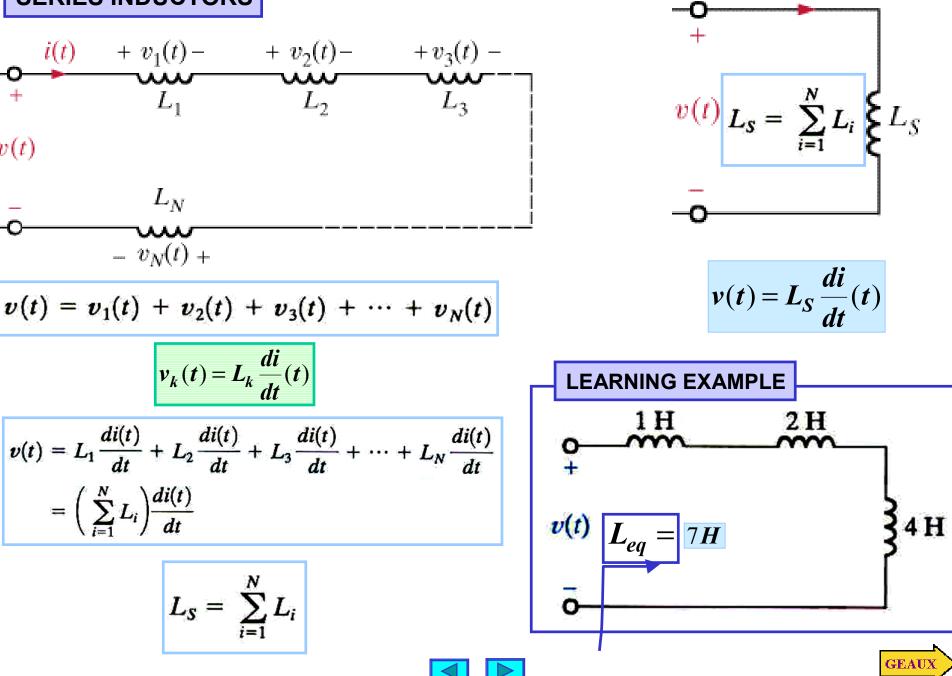
All capacitors are equal with C=8 microFarads





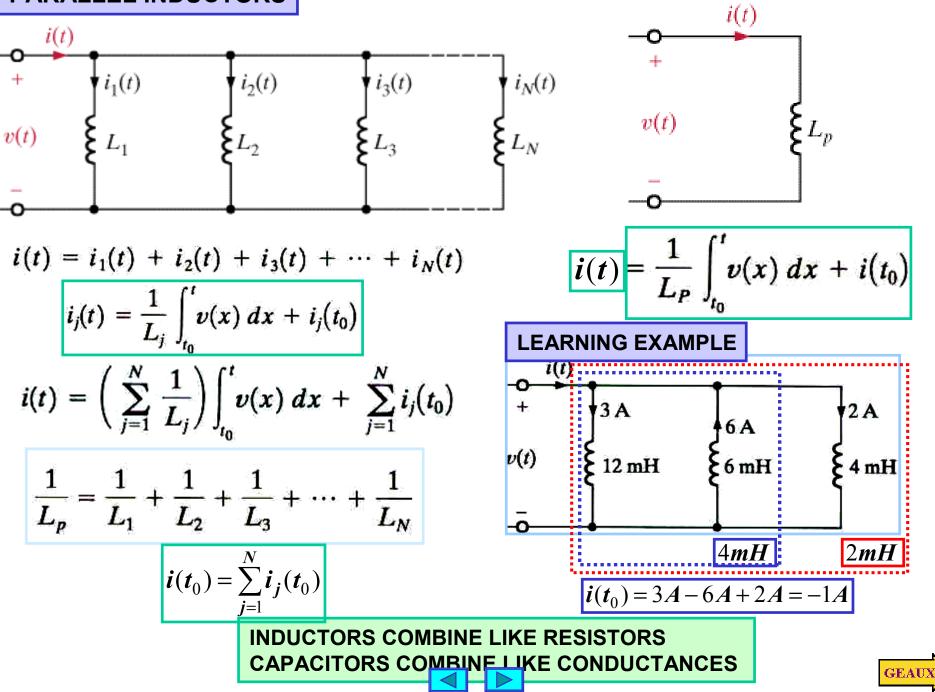


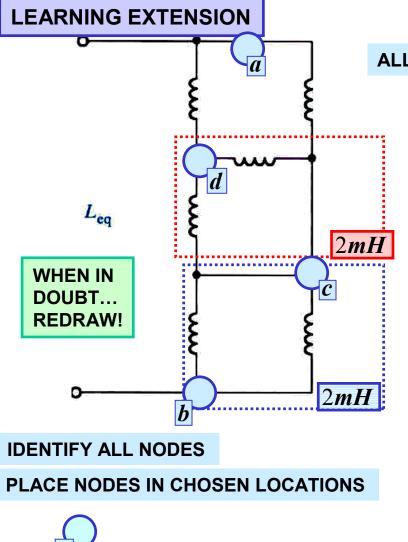
SERIES INDUCTORS



i(t)

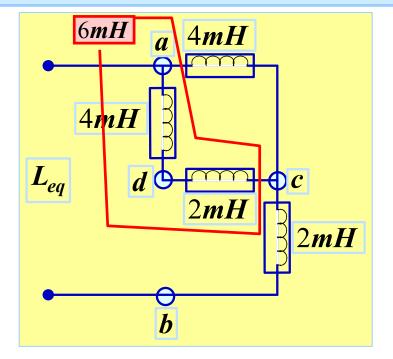
PARALLEL INDUCTORS





ALL INDUCTORS ARE 4mH

CONNECT COMPONENTS BETWEEN NODES



 $L_{eq} = (6mH \parallel 4mH) + 2mH = 4.4mH$



D

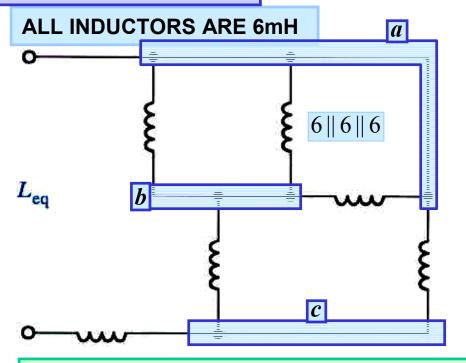


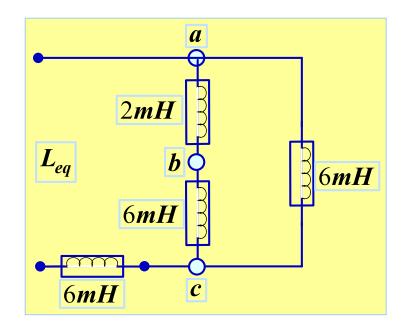






LEARNING EXTENSION





NODES CAN HAVE COMPLICATED SHAPES. KEEP IN MIND DIFFERENCE BETWEEN PHYSICAL LAYOUT AND ELECTRICAL CONNECTIONS

$$L_{eq} = 6 + [(6+2) || 6] = 6 + \frac{48}{14} = 6\frac{24}{7}mH$$

$$L_{eq} = \frac{66}{7} mH$$

a









SUMMARY

• The important (dual) relationships for capacitors and inductors are as follows:

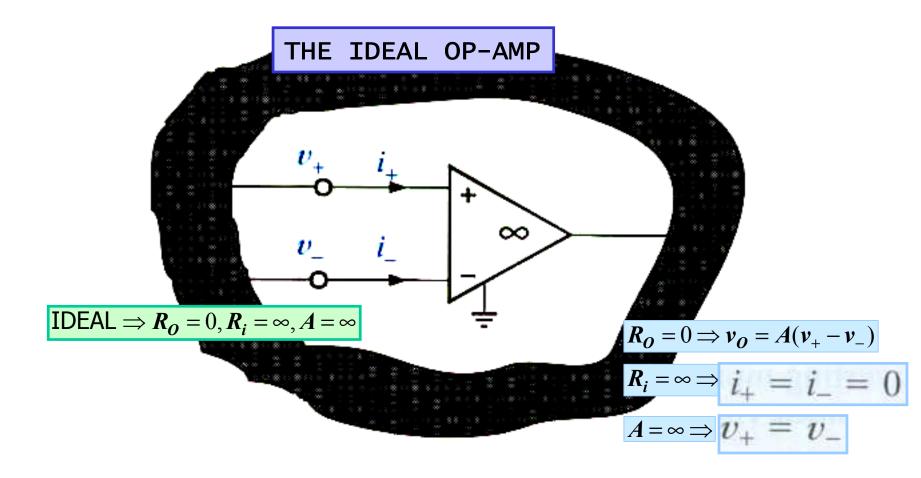
q = Cv	
$i(t) = C \frac{dv(t)}{dt}$	$v(t) = L \frac{di(t)}{dt}$
$v(t) = \frac{1}{C} \int_{-\infty}^{t} i(x) dx$	$i(t) = \frac{1}{L} \int_{-\infty}^{t} v(x) dx$
$p(t) = Cv(t) \frac{dv(t)}{dt}$	$p(t) = Li(t) \frac{di(t)}{dt}$
$W_C(t) = 1/2Cv^2(t)$	$W_L(t) = 1/2Li^2(t)$

- The passive sign convention is used with capacitors and inductors.
- In dc steady state a capacitor looks like an open circuit and an inductor looks like a short circuit.
- Leakage resistance is present in practical capacitors and inductors.
- When capacitors are interconnected, their equivalent capacitance is determined as follows: Capacitors in series combine like resistors in parallel and capacitors in parallel combine like resistors in series.
- When inductors are interconnected, their equivalent inductance is determined as follows: Inductors in series combine like resistors in series and inductors in parallel combine like resistors in parallel.
- *RC* operational amplifier circuits can be used to differentiate or integrate an electrical signal.



RC OPERATIONAL AMPLIFIER CIRCUITS

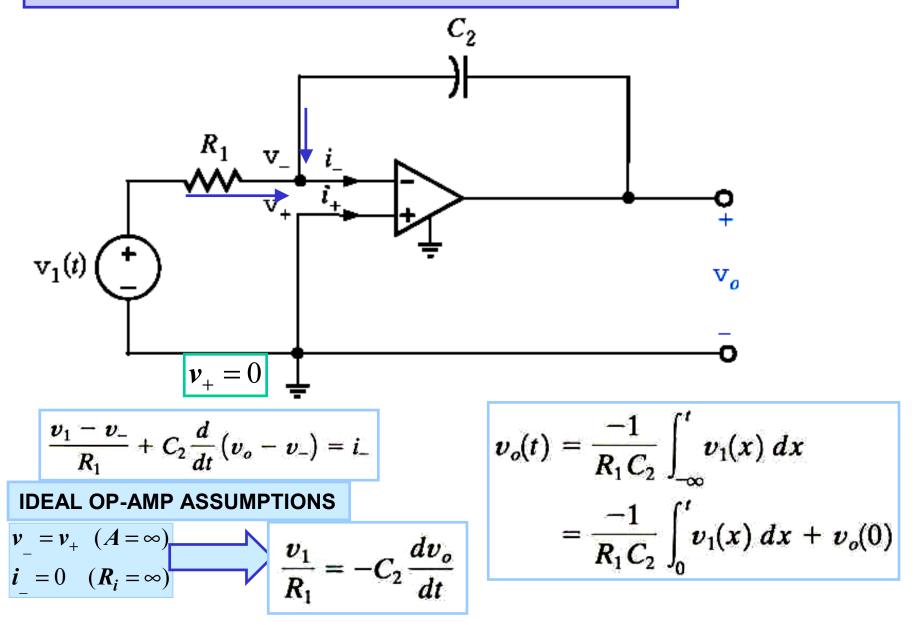
INTRODUCES TWO VERY IMPORTANT PRACTICAL CIRCUITS BASED ON OPERATIONAL AMPLIFIERS







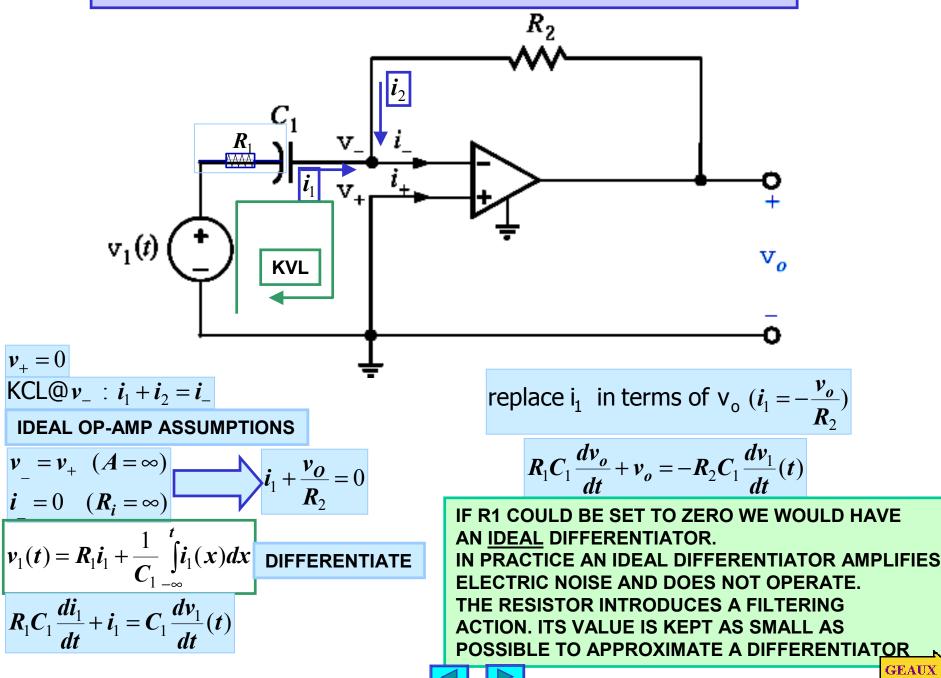
RC OPERATIONAL AMPLIFIER CIRCUITS -THE INTEGRATOR







RC OPERATIONAL AMPLIFIER CIRCUITS - THE DIFFERENTIATOR







UNMEASURABLE, SIGNALS. THESE UNDESIRED SIGNALS ARE REFERRED TO AS <u>NOISE</u>

SIMPLE MODEL FOR A NOISY 60Hz SINUSOID CORRUPTED WITH ONE MICROVOLT OF 1GHz INTERFERENCE.

$$y(t) = \sin(120\pi t) + 10^{-6}\sin(2\times 10^{9}\pi t)$$

noise

signal

$$\frac{\text{noise amplitude}}{\text{signal amplitude}} = 10^{-6}$$

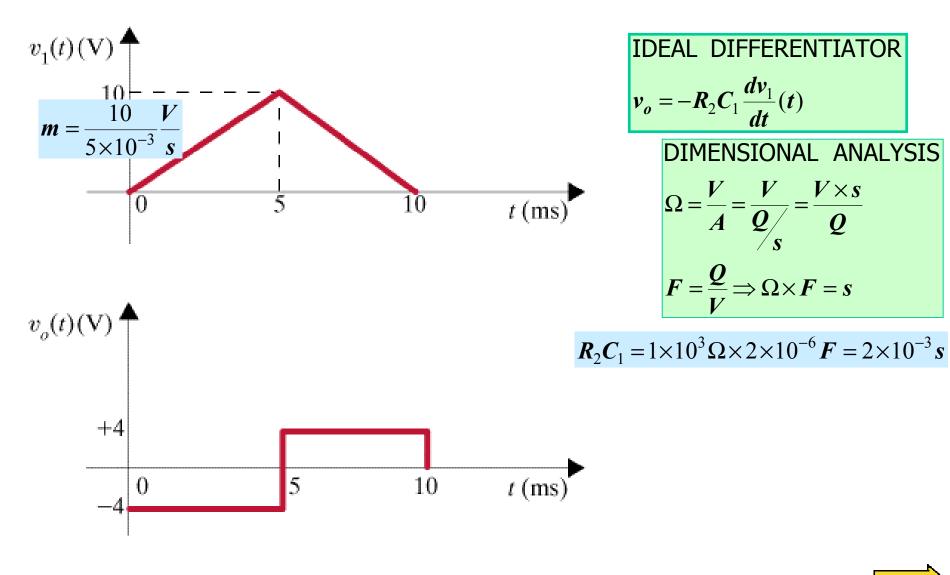
THE DERIVATIVE
$$\frac{dy}{dt}(t) = 120\pi \cos(120\pi) + 2000\pi \cos(2 \times 10^9 \pi t)$$
noise amplitude
signal amplitude = $\frac{2000}{120} = 16.67$ signalnoise





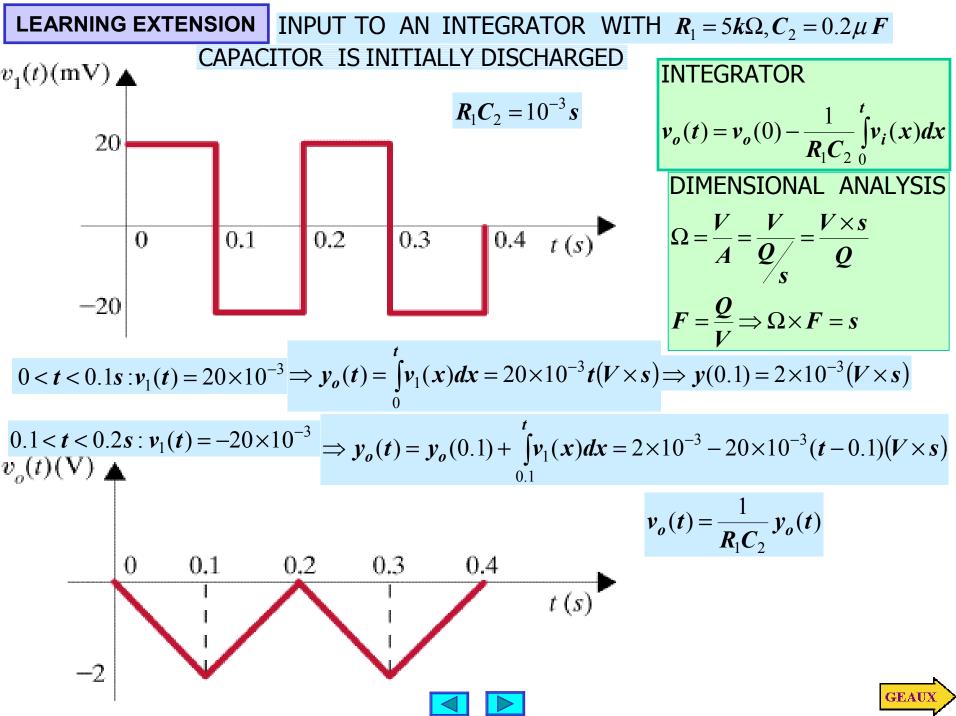
LEARNING EXTENSION

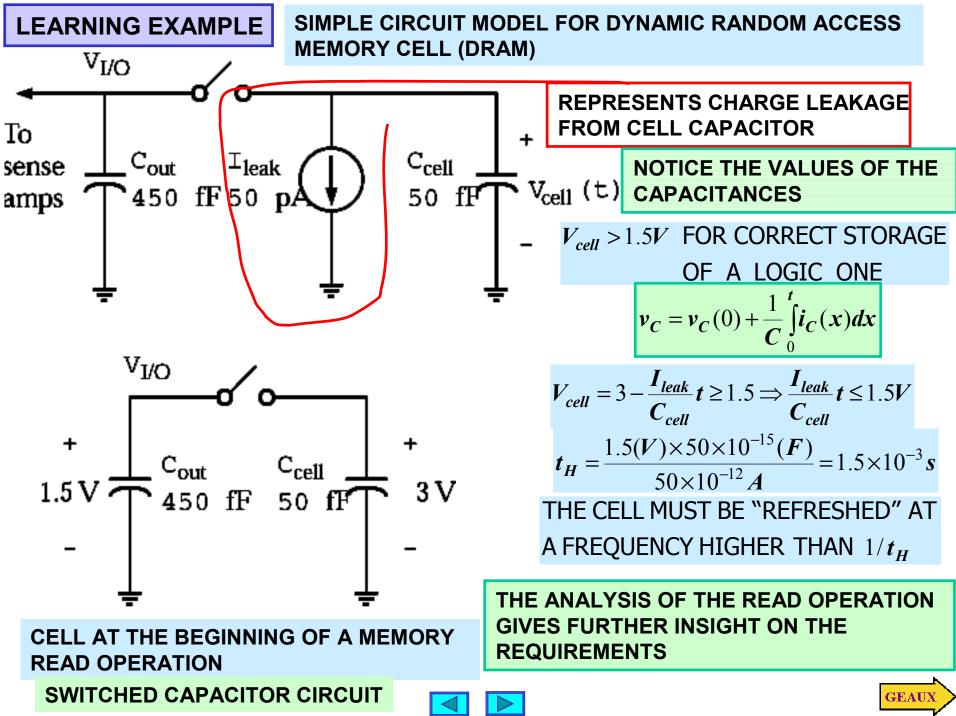
INPUT TO IDEAL DIFFERENTIATOR WITH $R_2 = 1k\Omega$, $C_1 = 2\mu F$

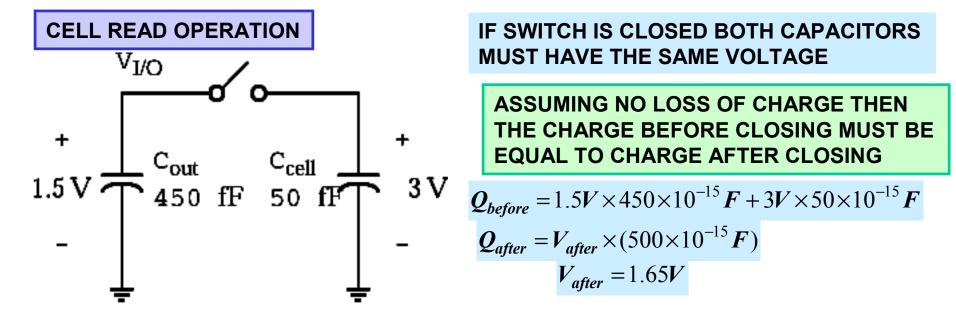




GEAU







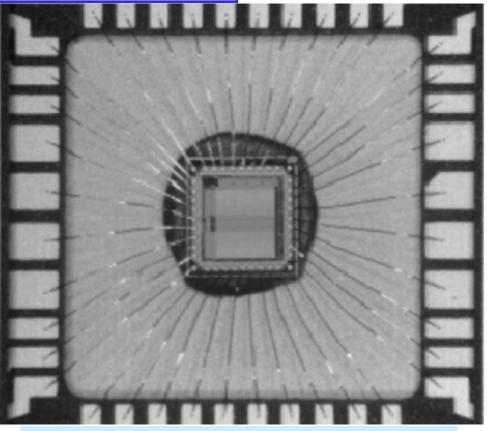
Even at full charge the voltage variation is small. SENSOR amplifiers are required

After a READ operation the cell must be refreshed





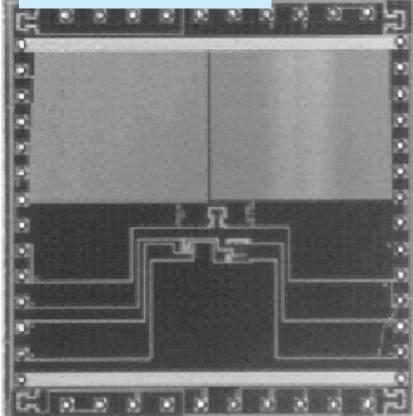
LEARNING EXAMPLE

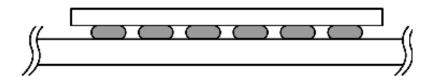


IC WITH WIREBONDS TO THE OUTSIDE

GOAL: REDUCE INDUCTANCE IN THE WIRING AND REDUCE THE "GROUND BOUNCE" EFFECT

FLIP CHIP MOUNTING

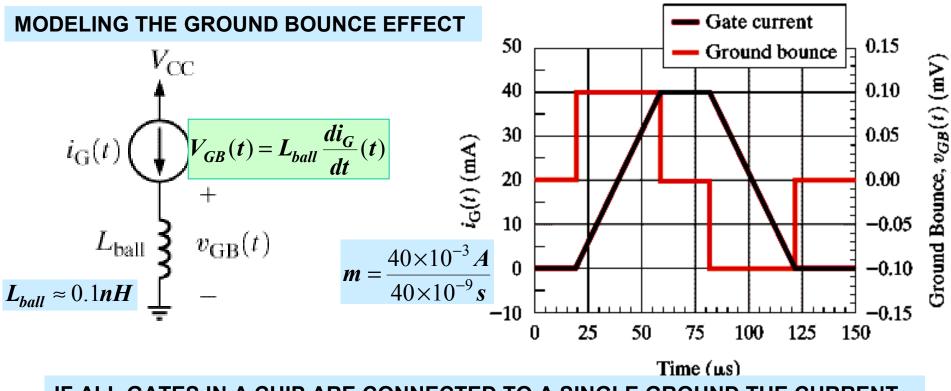




A SIMPLE MODEL CAN BE USED TO DESCRIBE GROUND BOUNCE







IF ALL GATES IN A CHIP ARE CONNECTED TO A SINGLE GROUND THE CURRENT CAN BE QUITE HIGH AND THE BOUNCE MAY BECOME UNACCEPTABLE

USE SEVERAL GROUND CONNECTIONS (BALLS) AND ALLOCATE A FRACTION OF THE GATES TO EACH BALL



