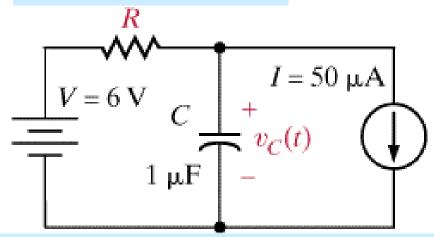


THE DISCHARGE STAGE



With the chosen resistor discharge starts after one second and the capacitor voltage is 5V

$$v_{C}(t) = K_{1} + K_{2}e^{-\frac{(t-1)}{\tau}}, t > 1$$

$$\frac{\tau = 0.569s}{v_{C}(1) = 5V}$$

$$v_{C}(\infty) = 6 - RI = 6 - 0.569 \times 10^{6} (\Omega) \times 50 \times 10^{-6}$$

$$K_{1} = -22.45$$

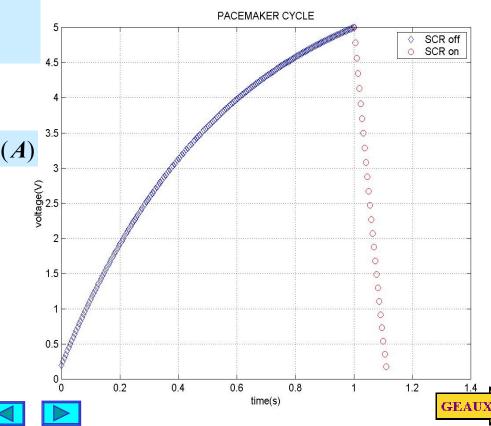
$$K_{1} + K_{2} = 5 \Rightarrow K_{2} = 27.45$$

$$v_{C}(t) = -22.45 + 27.45e^{-\frac{(t-1)}{0.569}} t > 1$$
For SCR turn off
$$v_{C}(1 + T_{off}) = 0.2$$

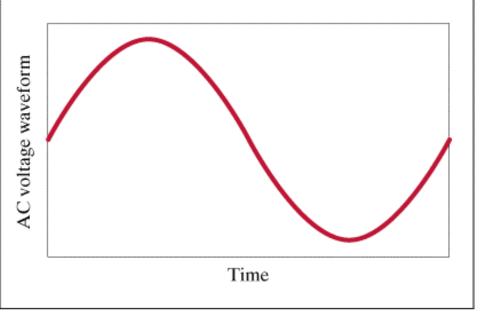
$$e^{\frac{T_{off}}{0.569}} = \frac{27.45}{22.65} \Rightarrow T_{off} = 0.11s$$

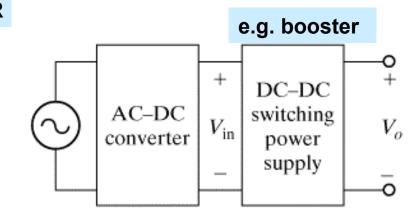
22.65

%example6p12 %visualizes one cycle of pacemaker %charge cycle tau=0.569; tc=linspace(0,1,200); vc=6-5.8*exp(-tc/tau); %discharge cycle. SCR on td=linspace(1,1.11,25); vcd=-22.45+27.45*exp(-(td-1)/tau); plot(tc,vc,'bd',td,vcd,'ro'),grid, title('PACEMAKER CYCLE') xlabel('time(s)'), ylabel('voltage(V)') legend('SCR off', 'SCR on')

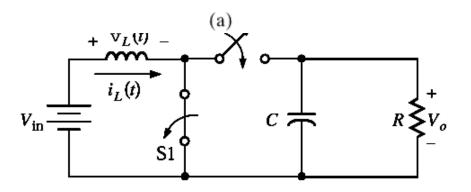


LEARNING EXAMPLE BOOSTER CONVERTER

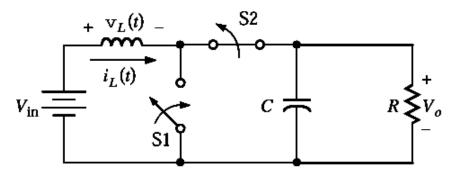




STANDARD DC POWER SUPPLY



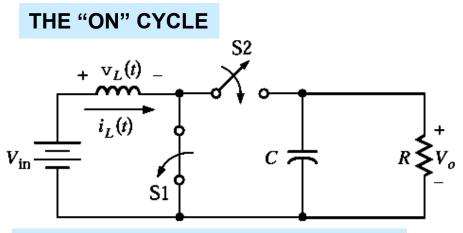
BOOSTER "ON" PERIOD Energy is stored in inductor. Capacitor discharges



BOOSTER "OFF" PERIOD Inductor releases energy. Capacitor charges

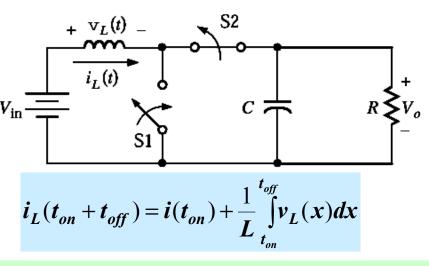
Inductor current at the beginning of ON period MUST be the same than the current at the end of OFF period





$$i_L(t_{on}) = i(0) + \frac{1}{L} \int_{0}^{t_{on}} v_L(x) dx = I_0 + \frac{V_{in}}{L} t_{on}$$

THE "OFF" CYCLE
$$t > t_{on}$$



SIMPLIFYING ASSUMPTION: THE OUTPUT VOLTAGE (Vo) IS CONSTANT

$$\boldsymbol{v}_L = \boldsymbol{V}_{in} - \boldsymbol{V}_0$$



$$I_{0} = i(t_{on}) + \frac{V_{in} - V_{0}}{L} t_{off}$$

$$I_{o} = I_{0} + \frac{V_{in}}{L} t_{on} + \frac{V_{in} - V_{0}}{L} t_{off}$$

$$V_{o} = \frac{t_{on} + t_{off}}{t_{off}} V_{in} \quad V_{0} > V_{in} \text{ (hence booster)}$$

$$Period: \quad T = t_{on} + t_{off}$$

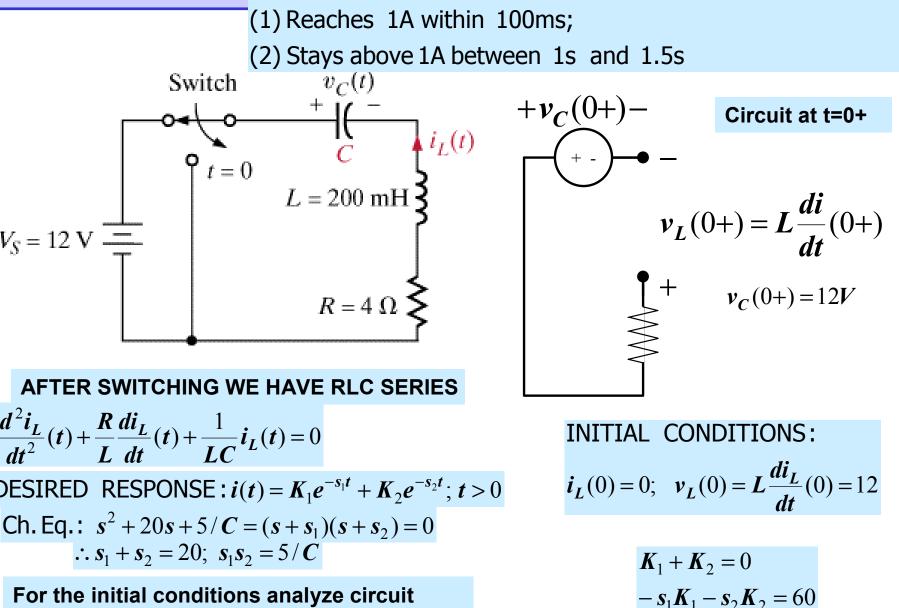
$$Duty cycle: D = \frac{t_{on}}{T}$$

$$\boldsymbol{V}_0 = \boldsymbol{V}_{in} \frac{1}{1 - \boldsymbol{D}}$$

By adjusting the duty cycle one can adjust the output voltage level



LEARNING BY DESIGN FIND C SUCH THAT i(t) IS OVERDAMPED, AND SATISFIES:



GEAUX

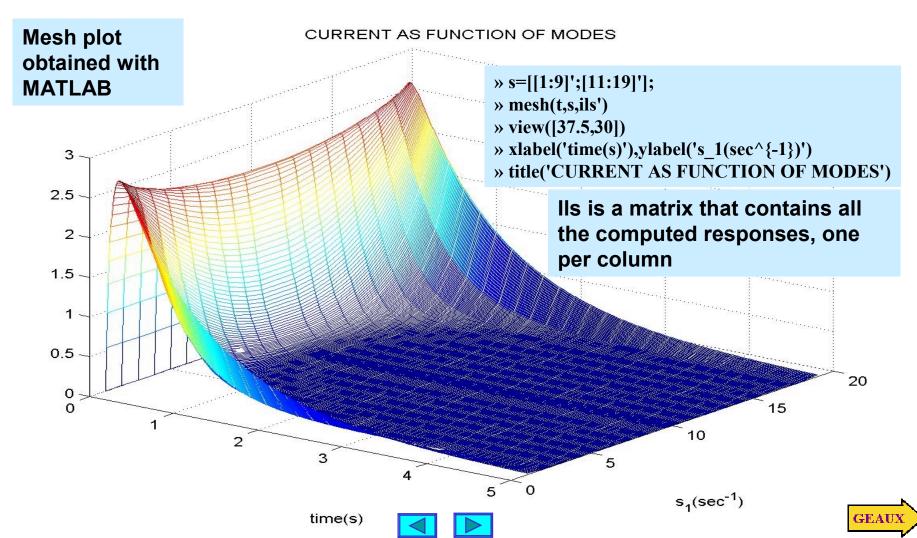
at t=0+. Assume the circuit was in steady

state prior to the switching

$$i_L(t) = \frac{60}{s_2 - s_1} \left(e^{-s_1 t} - e^{-s_2 t} \right)$$

NOW ONE CAN USE TRIAL AND ERROR OR CAN ATTEMPT TO ESTIMATE THE REQUIRED CAPACITANCE

IF FEASIBLE, GET AN IDEA OF THE FAMILY OF SOLUTIONS

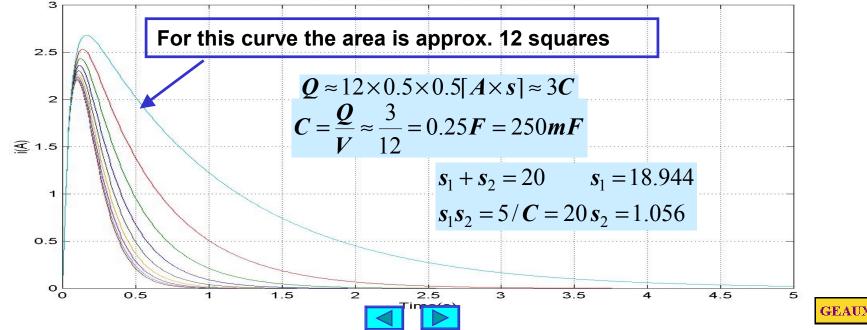


```
%example6p14.m
%displays current as function of roots in characteristic equation
% il(t)=(60/(s2-s1))*(exp(-s1*t)-exp(s2*t));
% with restriction s1+s2=20, s1~=s2.
t=linspace(0,5,500)'; %set display interval as a column vector
ils=[]; %reserve space to store curves
for s1=1:19
   s2=20-s1;
   if s1~=s2
      il=(60/(s2-s1))*(exp(-s1*t)-exp(-s2*t));
      ils=[ils il]; %save new trace as a column in matrix
end
```

end

%now with one command we plot all the columns as functions of time plot(t,ils), grid, xlabel('Time(s)'),ylabel('i(A)') title('CURRENT AS FUNCTION OF MODES')





%verification

```
s1=18.944;
s2=20-s1;
i1=(60/(s2-s1))*(exp(-s1*t)-exp(-s2*t));
plot(t,i1,'rd',t,i1,'b'), grid, xlabel('time(s)'), ylabel('i(A)')
title('VERIFICATION OF DESIGN')
```

VERIFICATION OF DESIGN 3 2.5 2 € 1.5 1 0.5 00 Applications 0.5 1.5 2 2.5 3.5 3 1 4 time(s)

