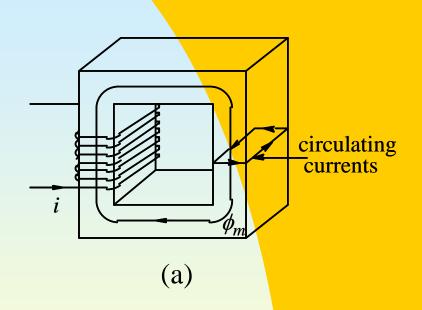
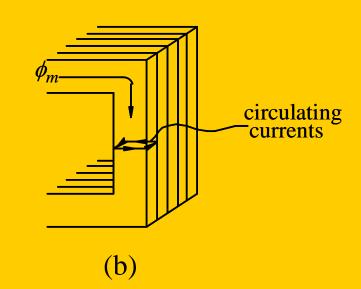
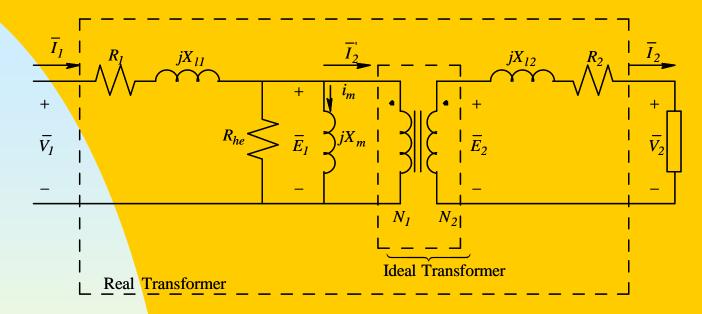
Eddy Current and Hysteresis Losses





- Laminating the core reduces Eddy-Current Losses
- Laminations 0.2 to 1 mm thick at 50/60 Hz
- Losses represented by a parallel Resistor

Obtaining Equivalent Circuit parameters



- **Open-Circuit Test**
 - Magnetizing Reactance and Core-Loss **Equivalent Resistance**
- Short-Circuit Test © Copyright Ned Mohan 2008- Leakage Impedances

Transformer Simplified Model

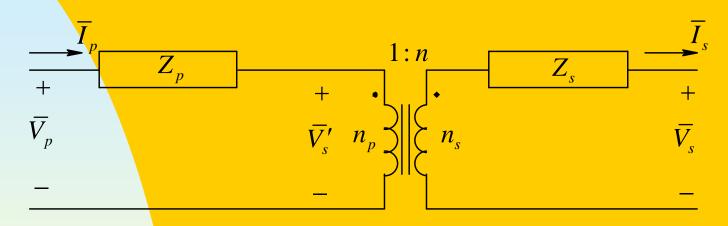


Fig. 6-7 Simplified transformer model.

$$n = n_s / n_p$$

Transferring Leakage Impedances from One Side to Another

$$n = n_{s} / n_{p}$$

$$1: n$$

$$\overline{V}_{p}$$

$$\overline{V}_{s}$$

$$-$$

$$1: n$$

$$\overline{V}_{s}$$

$$\overline{V}_{s}$$

$$-$$

$$-$$

$$1: n$$

$$\overline{V}_{s}$$

$$\overline{V}_{s}$$

$$-$$

$$-$$

$$\overline{V}_{s}' = \frac{Z_{s}\overline{I}_{s}}{n} \qquad \overline{V}_{p} = \overline{V}_{s}' + Z_{p}\overline{I}_{p}$$

$$\overline{I}_{s} = \overline{I}_{p}/n \qquad \overline{\overline{I}_{p}} = Z_{ps} = Z_{p} + (Z_{s}/n^{2})$$

$$\overline{V}_{s}' = (\frac{Z_{s}}{n^{2}})\overline{I}_{p}$$

Transformer Equivalent Circuit in Per Unit

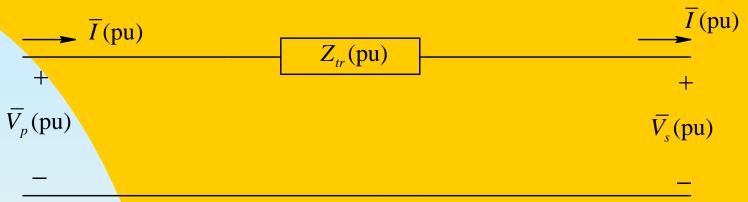


Fig. 6-9 Transformer equivalent circuit in per unit (pu).

$$\frac{V_{p\,,rated}}{V_{s\,,rated}} = \frac{1}{n} \qquad Z_{p\,,base} = V_{p\,,rated} / I_{p\,,rated}$$

$$\frac{I_{p\,,rated}}{I_{s\,,rated}} = n \qquad Z_{s\,,base} = V_{s\,,rated} / I_{s\,,rated}$$

$$\frac{Z_{p\,,base}}{Z_{s\,,base}} = \left(\frac{1}{n}\right)^2 \qquad Z_{tr}(\text{pu}) = Z_{ps}(\text{pu}) = Z_{sp}(\text{pu})$$
 © Copyright Ned Mohan 2008

Connection of Transformer Windings

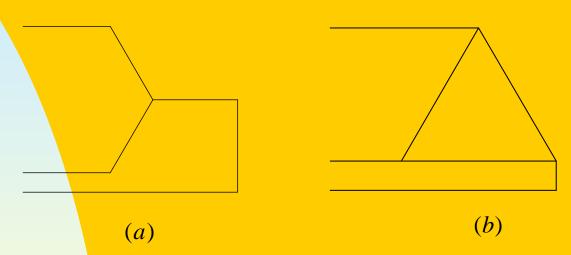


Fig. 6-10 Winding connections in a three-phase system.

Including Nominal Turns-Ratio Transformer in Power Flow Studies

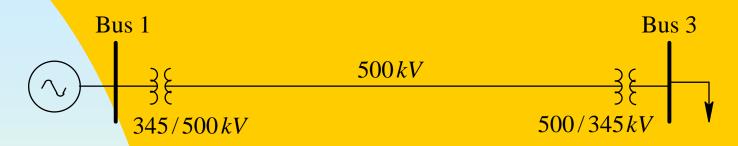


Fig. 6-11 Including nominal-voltage transformers in per-unit.

$$Z_{base}(\Omega) = \frac{kV_{base}^{2}(\text{L-L})}{MVA_{base}(\text{3-phase})}$$

$$Z_{pu}$$
 (new) = Z_{pu} (original) × $\frac{MVA_{base}$ (new) MVA_{base} (original)

Transformer Losses and Leakage Reactances

%Efficiency=
$$100 \times \frac{P_{output}}{P_{input}} = 100 \times \left(1 - \frac{P_{losses}}{P_{input}}\right)$$

- Winding resistance well below 5%
- Efficiencies in excess of 99.5%

Leakage Reactances

- 7% to 20%

Regulation in Transformers

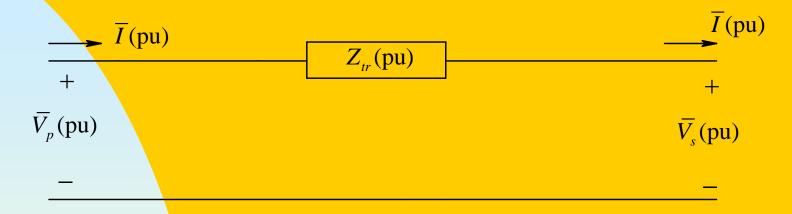


Fig. 6-9 Transformer equivalent circuit in per unit (pu).

$$\overline{V}_{s}(pu) = \overline{V}_{p}(pu) - jX_{tr}(pu)\overline{I}(pu)$$