

# Network Communications: Chapter 3

Introduction to Computer  
Architecture

# Components of a Microcomputer

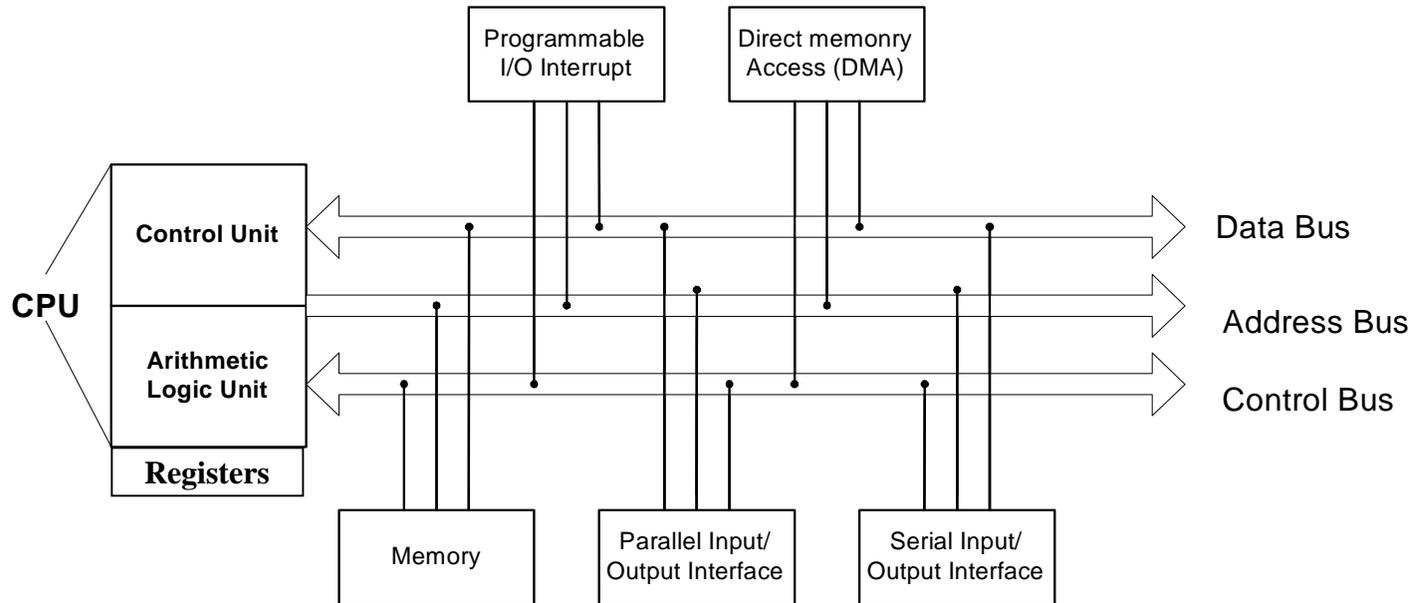


Figure 3.1

# Microprocessor and CPU Block Diagram

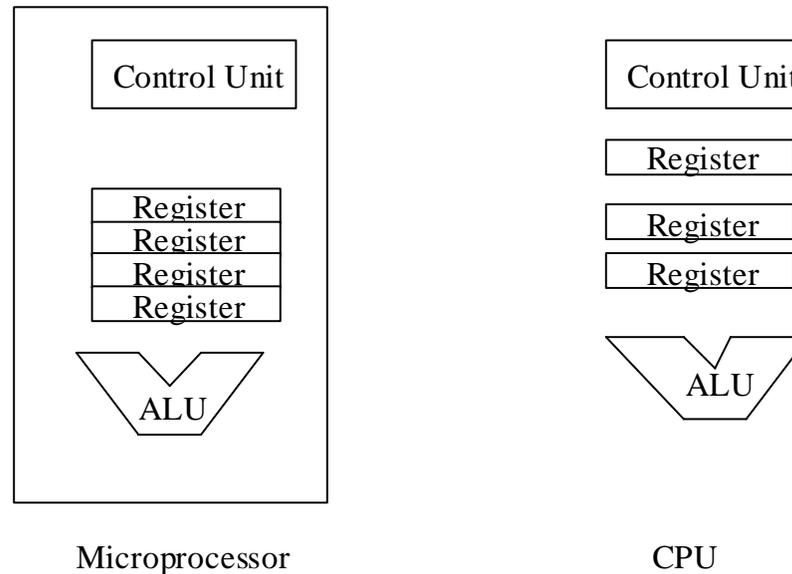


Figure 3.2

# RAM

## 3 Address Lines, 4 Data Lines

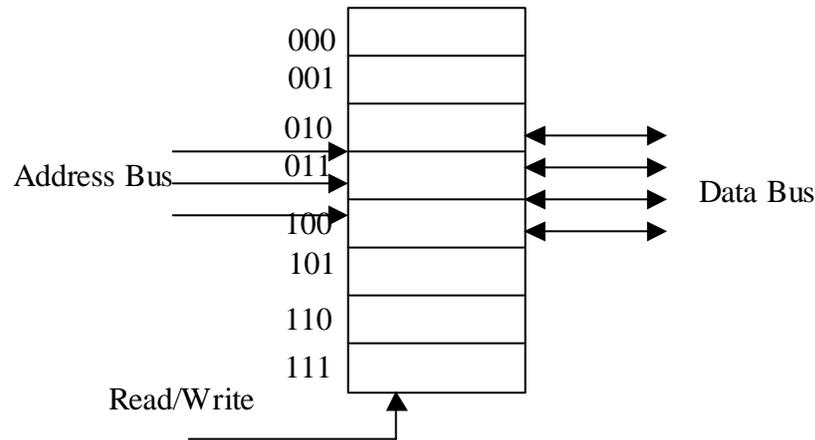


Figure 3.3

# RAM Block Diagram

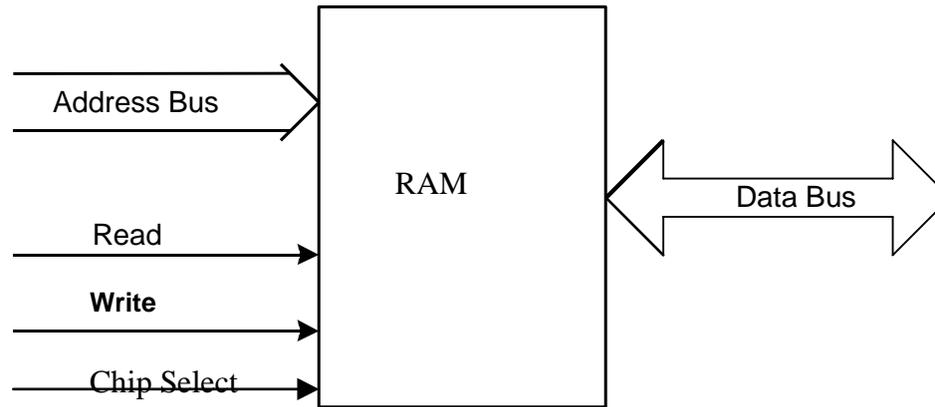


Figure 3.4

# SDRAM Block Diagram

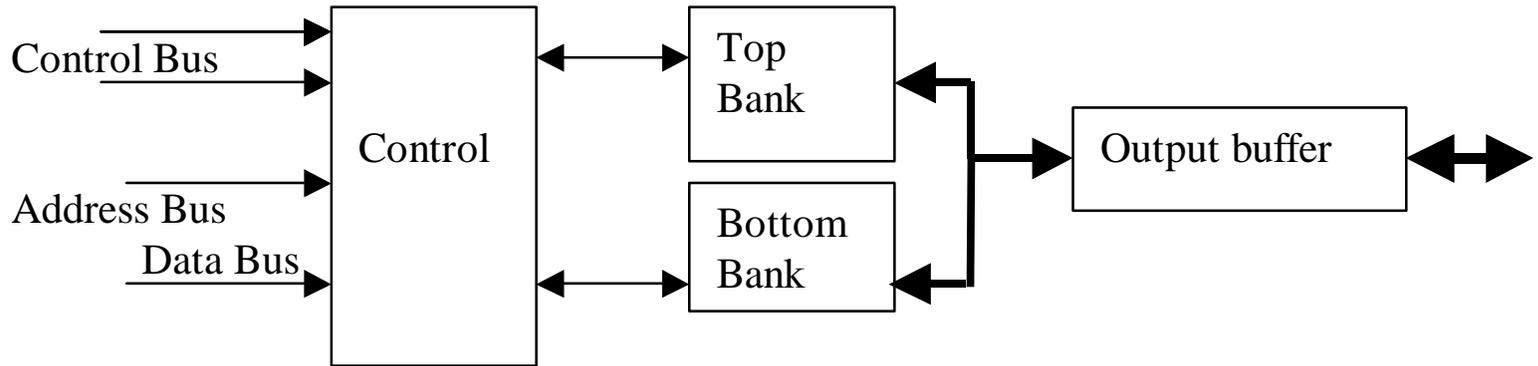


Figure 3.5

# DRAM Simm

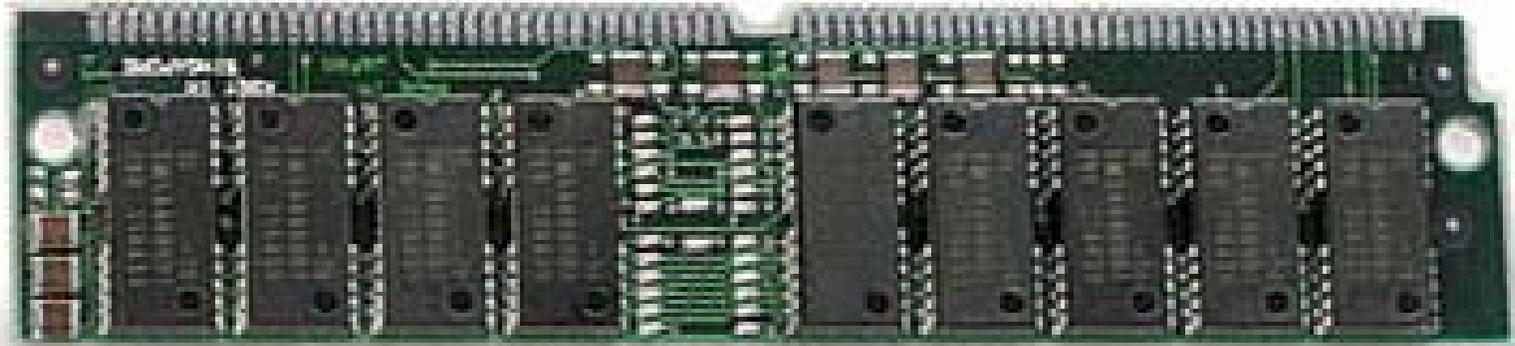


Figure 3.6

# Centronics Connectors



Centronics 36pin male

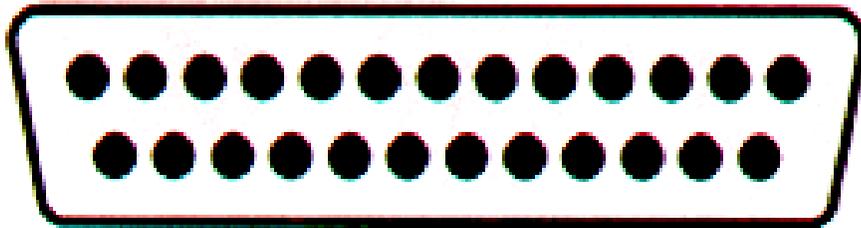


Centronics 34 pin female

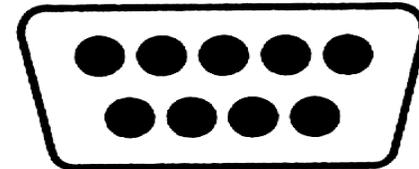
Figure 3.7

# Serial Ports

## RS232



DB-25



DB-9

Figure 3.8

# Memory Hierarchy

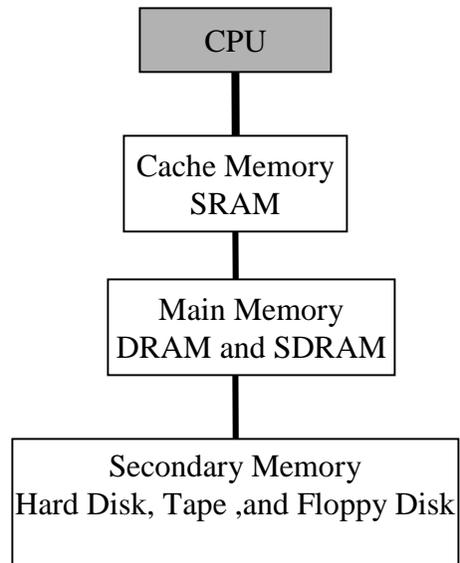


Figure 3.10

# SCSI Bus

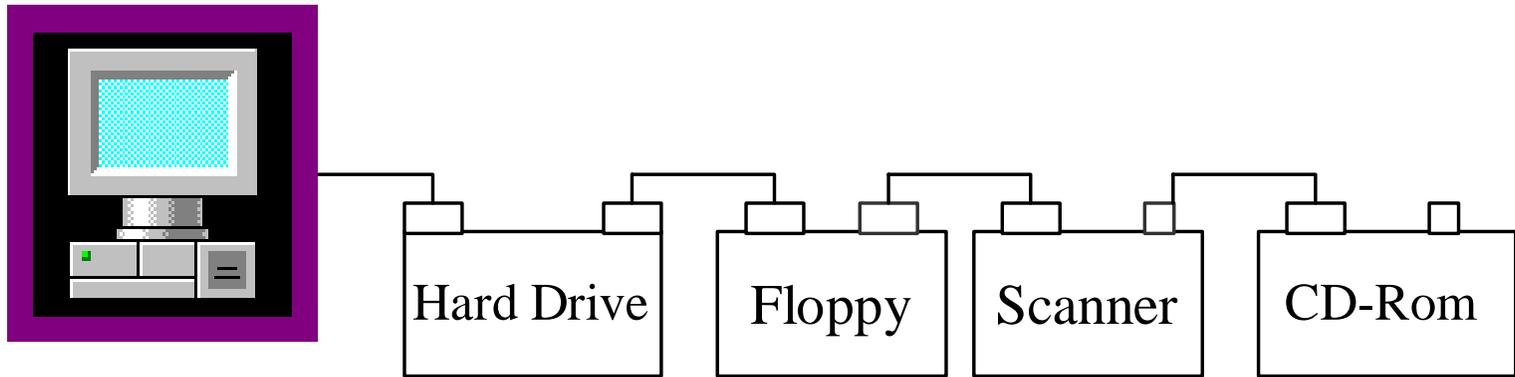
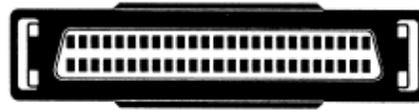


Figure 3.11

# SCSI Connectors



SCSI-1



SCSI-2



SCSI-3

Figure 3.12

# ISA Card

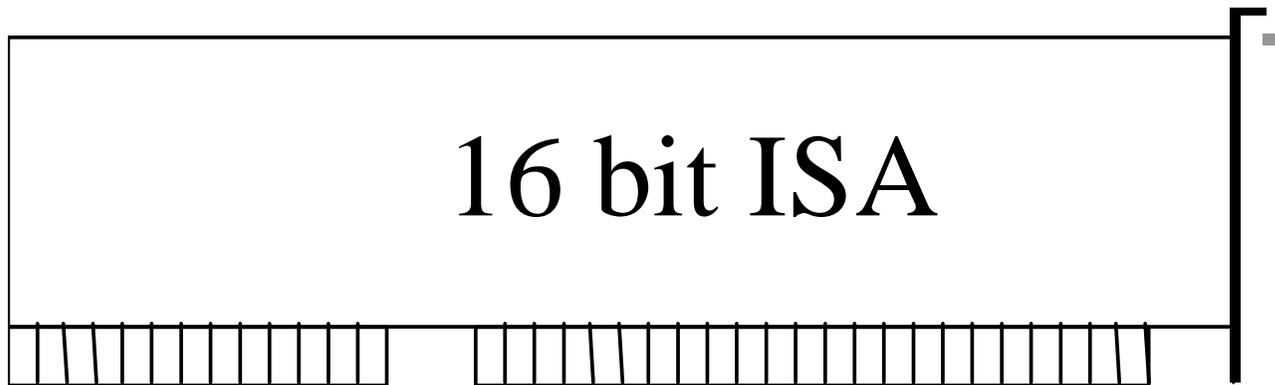


Figure 3.13

# EISA Card (Obsolete)

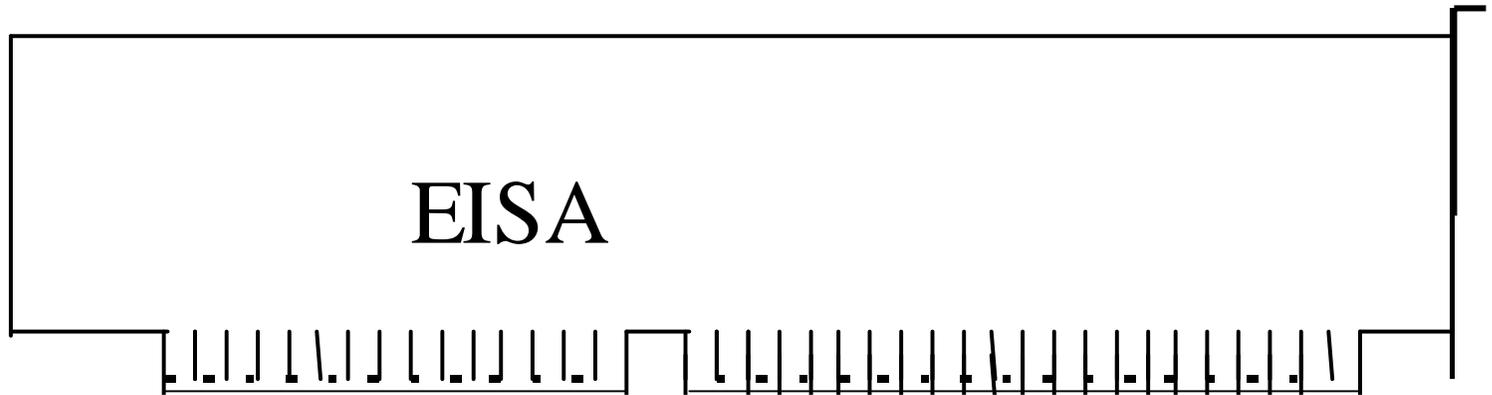


Figure 3.14

# PCI Card

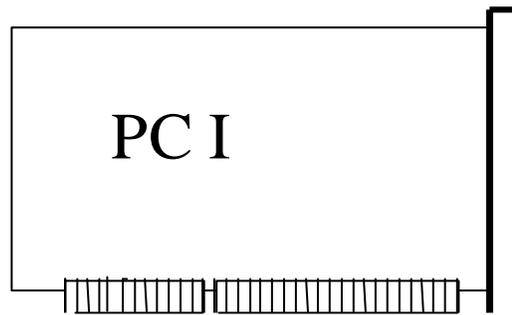


Figure 3.15

# PCMCIA PC Card

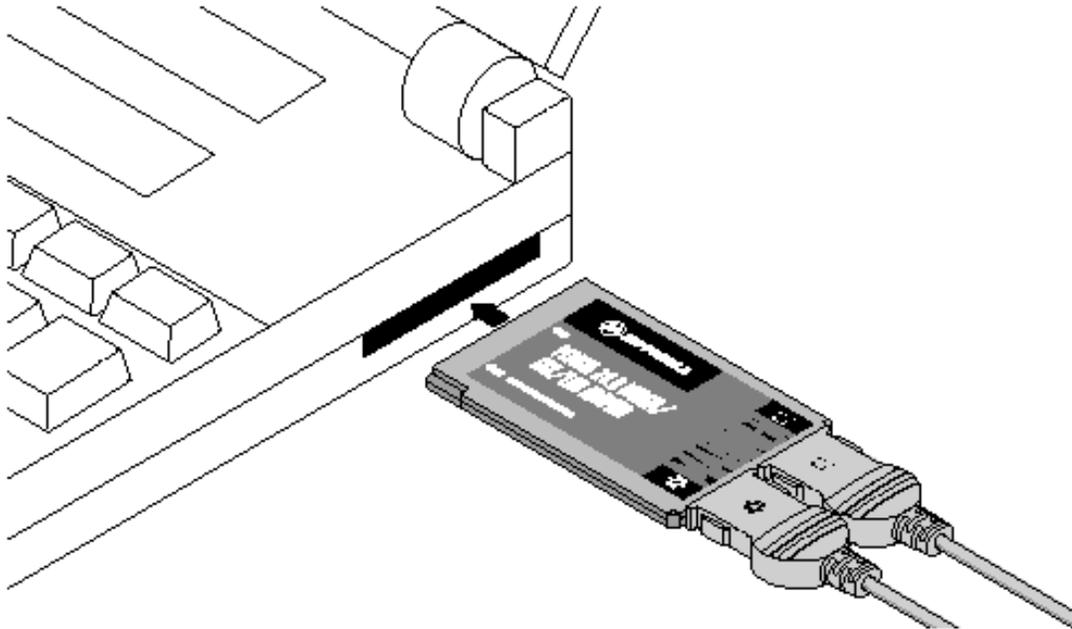


Figure 3.16

# USB Architecture

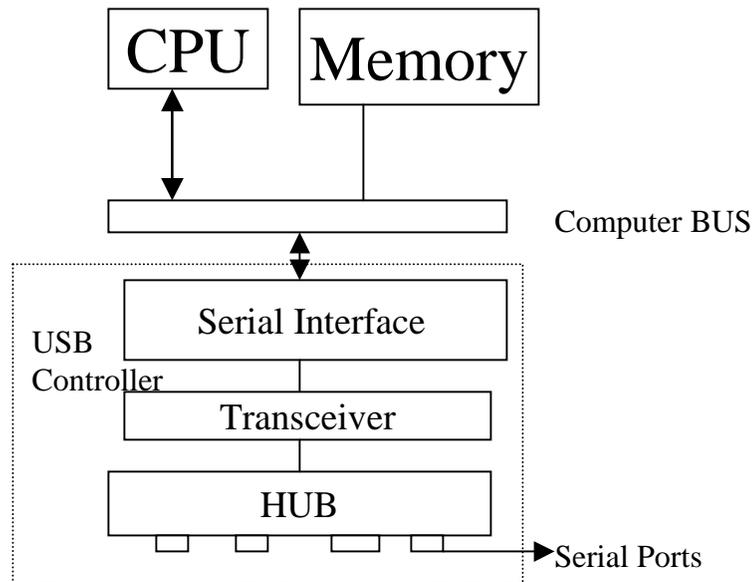


Figure 3.17

# USB Cable / Expansion

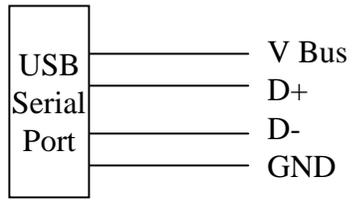


Figure 3.18

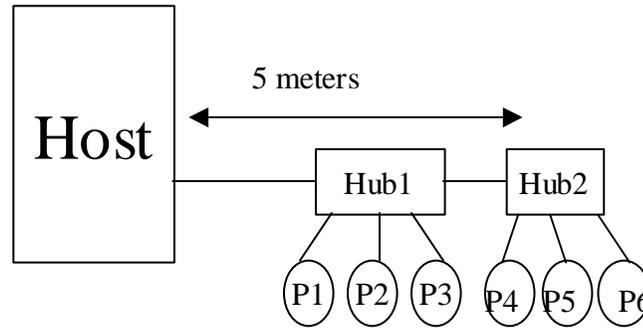


Figure 3.19